

SANYO Semiconductors DATA SHEET

TT2140LS — NPN Triple Diffused Planar Silicon Transistor Color TV Horizontal Deflection Output Applications

Features

- · High speed.
- High breakdown voltage (VCBO=1500V).
- · High reliability (Adoption of HVP process).
- · Adoption of MBIT process.
- · On-chip damper diode.

Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	VCBO		1500	V
Collector-to-Emitter Voltage	VCEO		800	V
Emitter-to-Base Voltage	VEBO		6	V
Collector Current	Ic		6	Α
Collector Current (Pulse)	ICP		15	Α
Collector Dissipation	PC		2.0	W
		Tc=25°C	30	W
Junction Temperature	Tj		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Electrical Characteristics at Ta=25°C

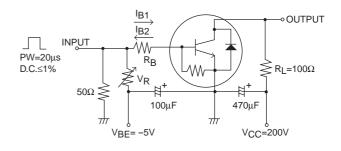
Parameter	Symbol	Conditions		Ratings		
			min	typ	max	Unit
Collector Cutoff Current	ICBO	VCB=800V, IE=0			10	μΑ
Emitter Cutoff Current	ICES	V _{CE} =1500V, R _{BE} =0			1.0	mA
Collector Sustain Voltage	VCEO(sus)	I _C =100mA, I _B =0	800			V
Emitter Cutoff Current	IEBO	VEB=4V, IC=0	40		130	mA
Collector-to-Emitter Saturation Voltage	V _{CE} (sat)	I _C =3.15A, I _B =0.63A			3	V
Base-to-Emitter Saturation Voltage	V _{BE} (sat)	I _C =3.15A, I _B =0.63A			1.5	V
DC Current Gain	hFE1	VCE=5V, IC=0.5A	10			
	hFE2	V _{CE} =5V, I _C =3.5A	5		8	
Diode Forward Voltage	VF	I _{EC} =6A			2	V
Fall Time	tf	IC=2A, IB1=0.4A, IB2=-0.8A			0.3	μs

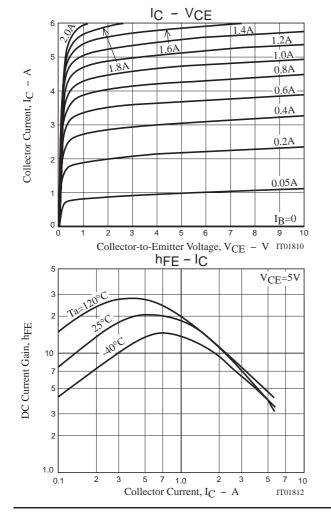
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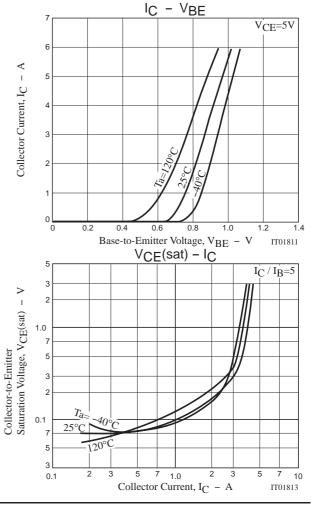
Package Dimensions

unit : mm 2079D

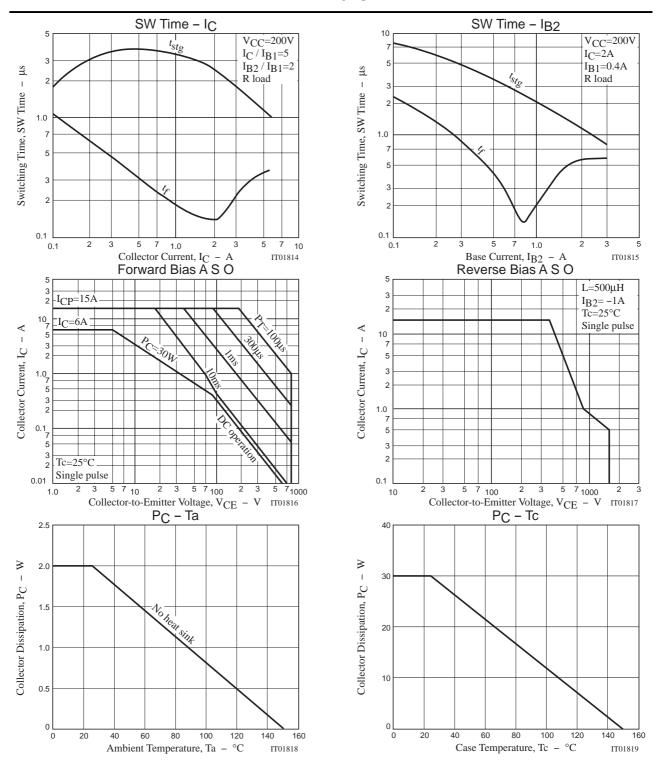
Switching Time Test Circuit







TT2140LS



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