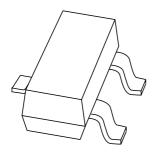
## **DISCRETE SEMICONDUCTORS**

## DATA SHEET



# **BCX70 series**NPN general purpose transistors

Product specification Supersedes data of 1999 Apr 15 2004 Jan 16





**Philips Semiconductors** 

## NPN general purpose transistors

## **BCX70** series

#### **FEATURES**

- Low current (max. 100 mA)
- Low voltage (max. 45 V).

## **APPLICATIONS**

• General purpose switching and amplification.

#### **DESCRIPTION**

NPN transistor in a SOT23 plastic package. PNP complements: BCX71 series.

#### **MARKING**

TYPE NUMBER	MARKING CODE(1)
BCX70G	AG*
BCX70H	AH*
BCX70J	AJ*
BCX70K	AK*

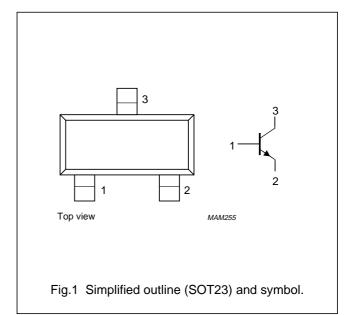
#### Note

1. \* = p : Made in Hong Kong.

\* = t : Made in Malaysia. \* = W : Made in China.

## ORDERING INFORMATION

PIN	DESCRIPTION
1	base
2	emitter
3	collector



TYPE		PACKAGE		
NUMBER	NAME DESCRIPTION			
BCX70G	_	plastic surface mounted package; 3 leads	SOT23	
BCX70H	]			
BCX70J				
BCX70K	1			

**PINNING** 

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## **LIMITING VALUES**

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V <sub>CBO</sub>	collector-base voltage	open emitter	_	45	V
V <sub>CEO</sub>	collector-emitter voltage	open base	_	45	V
V <sub>EBO</sub>	emitter-base voltage	open collector	_	5	V
I <sub>C</sub>	collector current (DC)		_	100	mA
I <sub>CM</sub>	peak collector current		_	200	mA
I <sub>BM</sub>	peak base current		_	200	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	_	250	mW
T <sub>stg</sub>	storage temperature		-65	+150	°C
Tj	junction temperature		_	150	°C
T <sub>amb</sub>	operating ambient temperature		-65	+150	°C

## THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	note 1	500	K/W

#### Note

1. Transistor mounted on an FR4 printed-circuit board.

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## **CHARACTERISTICS**

 $T_{amb}$  = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I <sub>CBO</sub>	collector cut-off current	I <sub>E</sub> = 0; V <sub>CB</sub> = 45 V	_	1-	20	nA
		I <sub>E</sub> = 0; V <sub>CB</sub> = 45 V; T <sub>amb</sub> = 150 °C	_	_	20	μΑ
I <sub>EBO</sub>	emitter cut-off current	I <sub>C</sub> = 0; V <sub>EB</sub> = 4 V	_	-	20	nA
h <sub>FE</sub>	DC current gain	$I_C = 10 \mu\text{A};  V_{CE} = 5 \text{V}$				
	BCX70G		_	_	_	
	всх70Н		40	_	_	
	BCX70J		30	-	_	
	BCX70K		100	-	_	
	DC current gain	$I_C = 2 \text{ mA}; V_{CE} = 5 \text{ V}$				
	BCX70G		120	_	220	
	всх70Н		180	-	310	
	BCX70J		250	-	460	
	всх70К		380	_	630	
	DC current gain	I <sub>C</sub> = 50 mA; V <sub>CE</sub> = 1 V				
	BCX70G		50	-	_	
	всх70Н		70	_	_	
	BCX70J		90	-	_	
	BCX70K		100	_	_	
V <sub>CEsat</sub>	collector-emitter saturation	I <sub>C</sub> = 10 mA; I <sub>B</sub> = 0.25 mA	50	1-	350	mV
	voltage	I <sub>C</sub> = 50 mA; I <sub>B</sub> = 1.25 mA	100	1-	550	mV
V <sub>BEsat</sub>	base-emitter saturation voltage	I <sub>C</sub> = 10 mA; I <sub>B</sub> = 0.25 mA	600	Ī-	850	mV
		I <sub>C</sub> = 50 mA; I <sub>B</sub> = 1.25 mA	700	-	1050	mV
V <sub>BE</sub>	base-emitter voltage	$I_C = 10 \mu\text{A};  V_{CE} = 5 \text{V}$	_	520	_	mV
		I <sub>C</sub> = 2 mA; V <sub>CE</sub> = 5 V	550	650	750	mV
		I <sub>C</sub> = 50 mA; V <sub>CE</sub> = 1 V	_	780	_	mV
C <sub>c</sub>	collector capacitance	I <sub>E</sub> = i <sub>e</sub> = 0; V <sub>CB</sub> = 10 V; f = 1 MHz	_	1.7	_	pF
C <sub>e</sub>	emitter capacitance	$I_C = i_c = 0$ ; $V_{EB} = 0.5 \text{ V}$ ; $f = 1 \text{ MHz}$	_	11	-	pF
f <sub>T</sub>	transition frequency	$I_C = 10 \text{ mA}$ ; $V_{CE} = 5 \text{ V}$ ; $f = 100 \text{ MHz}$ ; note 1	100	250	_	MHz
F	noise figure	$I_C = 200 \ \mu A; \ V_{CE} = 5 \ V; \ R_S = 2 \ k\Omega; \ f = 1 \ kHz; \ B = 200 \ Hz$	_	2	6	dB

## Note

1. Pulse test:  $t_p \le 300 \ \mu s; \ \delta \le 0.02.$ 

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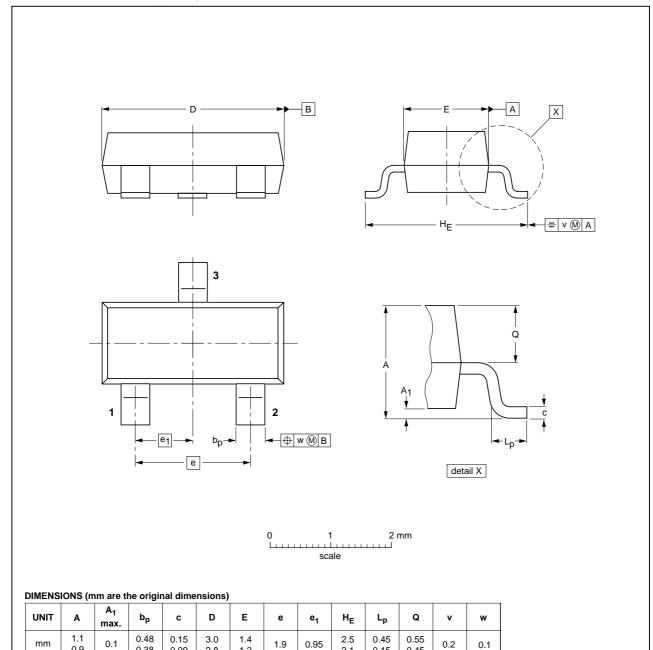
## NPN general purpose transistors

## BCX70 series

## **PACKAGE OUTLINE**

## Plastic surface mounted package; 3 leads

SOT23



REFERENCES			EUROPEAN	ISSUE DATE	
IEC	JEDEC	EIAJ		PROJECTION	ISSUE DATE
	TO-236AB				<del>-97-02-28-</del> 99-09-13
	IEC	IEC JEDEC	IEC JEDEC EIAJ	IEC JEDEC EIAJ	IEC JEDEC EIAJ PROJECTION

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## NPN general purpose transistors

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LEVEL	DATA SHEET STATUS <sup>(1)</sup>	PRODUCT STATUS(2)(3)	DEFINITION
I	Objective data	Development	This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice.
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