



**Macroblock**

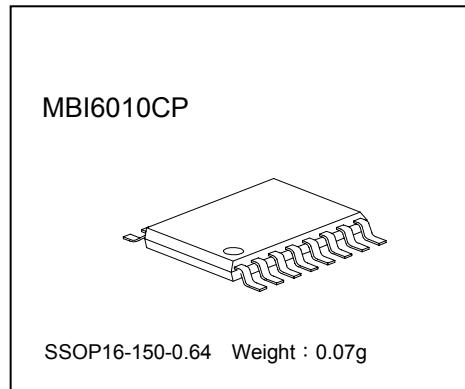
Advance Information

**MBI6010**

## 3-Channel Driver for RGB LED Cluster

### Features

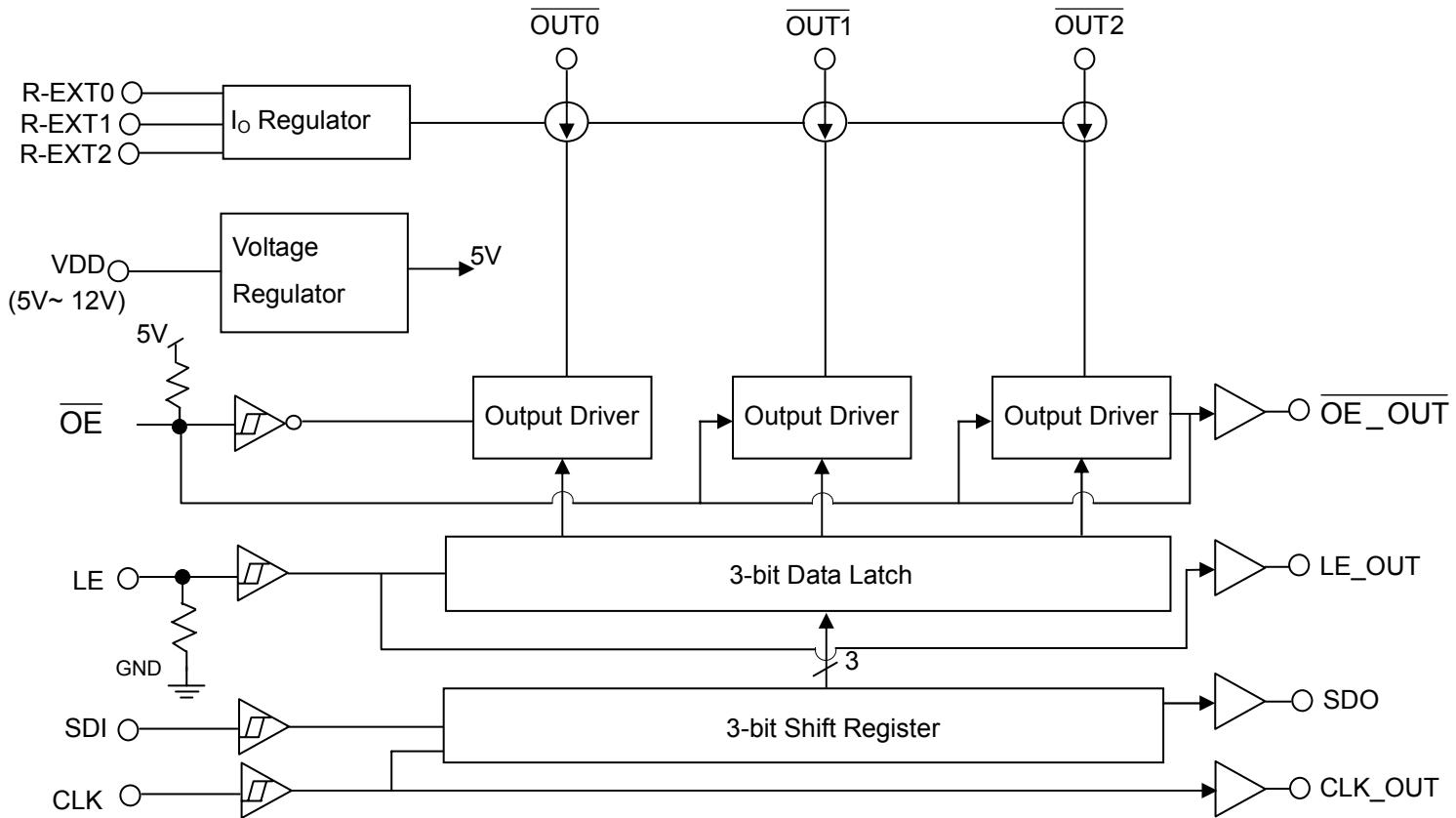
- 3 constant current channels for RGB LED cluster
- Output current invariant to load voltage change: 5V~ 12V
- Constant output current range: 3~ 60mA
- Output buffers for CLK, SDI, LE, and  $\overline{OE}$
- Output current accuracy:  
between channels: <  $\pm 5\%$  (max.), and  
between ICs: <  $\pm 5\%$  (max.)
- Built-in voltage regulator for 5~ 12V supply voltage
- 5 MHz clock frequency



### Applications

- Ground/Wall indicator
- Architectural lighting
- Entertainment lighting
- City beautification
- Landscape lighting
- Signage/sign board

## Block Diagram



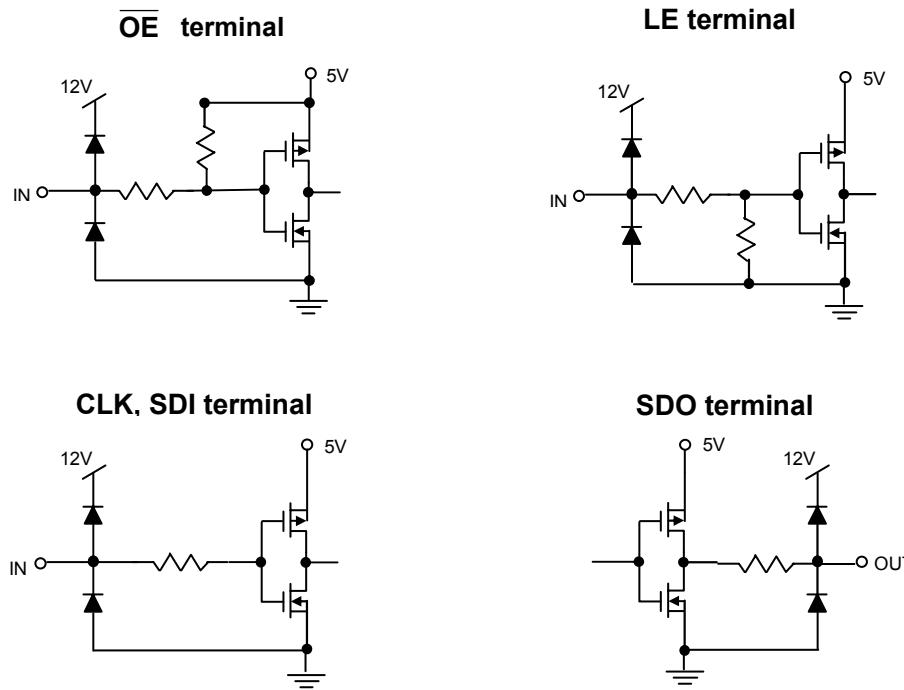
## Terminal Description

Pin No.	Pin Name	Function
1	GND	Ground terminal for control logic and current sink
2, 15	OE, OE_OUT	Output enable terminal When(active)low, the output drivers are enabled; when high, all output drivers are turned OFF (blanked)
3, 14	LE, LE_OUT	Data strobe input terminal Serial data is transferred to the output latch when LE is high. The data is latched when LE goes low.
4	SDI	Serial-data input to the shift register
5, 12	CLK, CLK_OUT	Clock input terminal for data shift on rising edge
6~8	R-EXT0, R-EXT1, R-EXT2	Input terminal used to connect an external resistor for setting up output current for all output channels
9~11	OUT0~OUT2	Constant current output terminal
13	SDO	Serial-data output to the following SDI of next driver IC
16	VDD	5- 12V supply voltage terminal

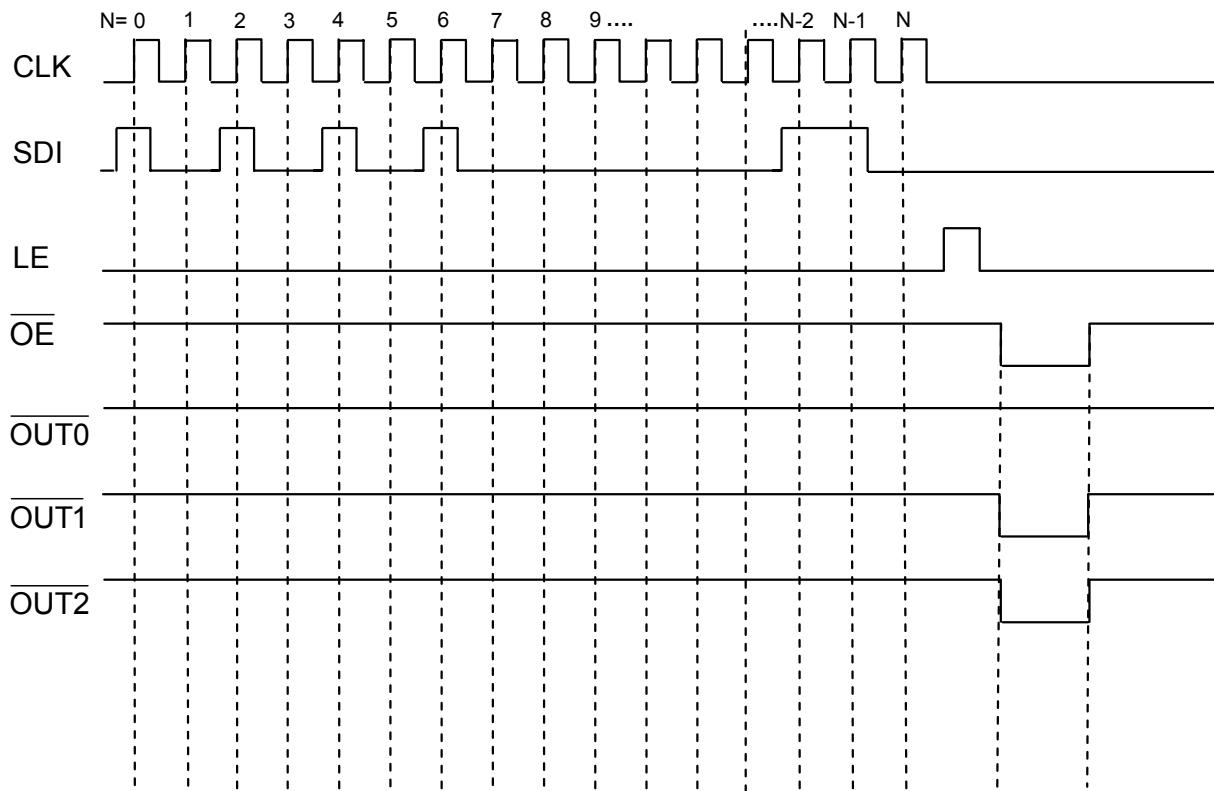
## Pin Configuration

GND	1	16	VDD
OE	2	15	OE_OUT
LE	3	14	LE_OUT
SDI	4	13	SDO
CLK	5	12	CLK_OUT
R-EXT2	6	11	OUT0
R-EXT1	7	10	OUT1
R-EXT0	8	9	OUT2

## Equivalent Circuits of Inputs and Outputs



## Timing Diagram



## Maximum Ratings

Characteristic	Symbol	Rating	Unit
Supply Voltage	V <sub>DD</sub>	5~12	V
Input Voltage	V <sub>IN</sub>	-0.4~V <sub>DD</sub> + 0.4	V
Output Current	I <sub>OUT</sub>	+60	mA
Output Voltage	V <sub>DS</sub>	-0.5~+12.0	V
GND Terminal Current	I <sub>GND</sub>	180	mA
Operating Temperature	T <sub>opr</sub>	-40~+85	°C
Storage Temperature	T <sub>stg</sub>	-55~+150	°C

## Electrical Characteristics

Characteristic	Symbol	Condition		Min.	Typ.	Max.	Unit
Supply Voltage	V <sub>DD</sub>	-		4.5	-	12	V
Output Voltage	V <sub>DS</sub>	$\overline{\text{OUT}0} \sim \overline{\text{OUT}2}$		-	-	17.0	V
Output Current		I <sub>OUT</sub>	DC Test Circuit	3	-	60	mA
		I <sub>OH</sub>	SDO, LE_OUT, OE_OUT, CLK_OUT	-	-	TBD	mA
		I <sub>OL</sub>	SDO, LE_OUT, OE_OUT, CLK_OUT	-	-	TBD	mA
Input Voltage	"H" level	V <sub>IH</sub>	Ta = -40~85°C	0.8V <sub>DD</sub>	-	V <sub>DD</sub>	V
	"L" level	V <sub>IL</sub>	Ta = -40~85°C	GND	-	0.3V <sub>DD</sub>	V
Output Leakage Current	I <sub>OH</sub>	V <sub>OH</sub> =17.0V		-	-	0.5	µA
Output Voltage	SDO, LE_OUT, OE_OUT, CLK_OUT	V <sub>OL</sub>	I <sub>OL</sub> =+1.0mA	-	-	0.4	V
		V <sub>OH</sub>	I <sub>OH</sub> =-1.0mA	4.6	-	-	V
Output Current 1	I <sub>OUT1</sub>	V <sub>DS</sub> =0.6V	R <sub>ext</sub> =TBD	-	26.25	-	mA
Current Skew	dI <sub>OUT1</sub>	I <sub>OL</sub> =26.25mA V <sub>DS</sub> =0.6V	R <sub>ext</sub> =TBD	-	-	±5	%
Output Current 2	I <sub>OUT2</sub>	V <sub>DS</sub> =0.8V	R <sub>ext</sub> =TBD	-	52.5	-	mA
Current Skew	dI <sub>OUT2</sub>	I <sub>OL</sub> =52.5mA V <sub>DS</sub> =0.8V	R <sub>ext</sub> =TBD	-	-	±5	%
Output Current vs. Output Voltage Regulation	%/dV <sub>DS</sub>	V <sub>DS</sub> within 1.0V and 3.0V		-	±0.1	-	% / V
Output Current vs. Supply Voltage Regulation	%/dV <sub>DD</sub>	V <sub>DD</sub> within 4.5V and 12V		-	±1	-	% / V
Pull-up Resistor	R <sub>IN</sub> (up)	$\overline{\text{OE}}$		250	500	800	KΩ
Pull-down Resistor	R <sub>IN</sub> (down)	LE		250	500	800	KΩ