500 mA Negative Voltage Regulators

The MC79M00 series of fixed output negative voltage regulators are intended as complements to the popular MC78M00 series devices.

Available in fixed output voltage options of -5.0 V, -8.0 V, -12 V and -15 V, these regulators employ current limiting, thermal shutdown, and safe-area compensation, making them remarkably rugged under most operating conditions. With adequate heatsinking they can deliver output currents in excess of 0.5 A.

- No External Components Required
- Internal Thermal Overload Protection
- Internal Short Circuit Current Limiting
- Output Transistor Safe–Area Compensation
- Also Available in Surface Mount DPAK (DT) Package
- Pb–Free Packages are Available

DEVICE TYPE/NOMINAL OUTPUT VOLTAGE

Device	Nominal Output Voltage
MC79M05	-5.0 V
MC79M08	-8.0 V
MC79M12	-12 V
MC79M15	-15 V







ON Semiconductor®

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THREE-TERMINAL NEGATIVE FIXED VOLTAGE REGULATORS

STANDARD APPLICATION



A common ground is required between the input and the output voltages. The input voltage must remain typically 1.1 V more negative even during the high point of the input ripple voltage. XX These two digits of the type number indicate nominal voltage.

* C_{in} is required if regulator is located an appreciable distance from power supply filter. ** C_{Ω} improve stability and transient response.



ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 5 of this data sheet.

MAXIMUM RATINGS (T_A = 25°C, unless otherwise noted.)

Rating		Value	Unit
Input Voltage	VI	-35	Vdc
Power Dissipation			
Case 221A (TO-220-3)			
$T_A = 25^{\circ}C$	PD	Internally Limited	W
Thermal Resistance, Junction-to-Ambient	θ_{JA}	65	°C/W
Thermal Resistance, Junction-to-Case	θ_{JC}	5.0	°C/W
Case 369C (DPAK-3)			
$T_A = 25^{\circ}C$	PD	Internally Limited	W
Thermal Resistance, Junction-to-Ambient	θ_{JA}	92	°C/W
Thermal Resistance, Junction-to-Case	θ_{JC}	6.0	°C/W
Storage Junction Temperature	T _{stg}	-65 to +150	°C
Operating Junction Temperature Range	ТJ	-40 to +150	°C

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected. *This device series contains ESD protection and exceeds the following tests:

Human Body Model 2000 V per MIL_STD_883, Method 3015 Machine Model Method 200 V

MC79M05B, C **ELECTRICAL CHARACTERISTICS** (V_I = -10 V, I_O = 350 mA, T_{low} to T_{high} (Note 2), unless otherwise noted.)

Characteristic	Symbol	Min	Тур	Max	Unit
Output Voltage ($T_J = 25^{\circ}C$)	V _O	-4.8	-5.0	-5.2	Vdc
$ \begin{array}{l} \mbox{Line Regulation, } T_J = 25^\circ C \ (\mbox{Note 1}) \\ -7.0 \ \mbox{Vdc} \geq V_I \geq -25 \ \mbox{Vdc} \\ -8.0 \ \mbox{Vdc} \geq V_I \geq -18 \ \mbox{Vdc} \end{array} $	Reg _{line}		7.0 2.0	50 30	mV
Load Regulation, $T_J = 25^{\circ}C$ (Note 1) 5.0 mA $\leq I_O \leq 500$ mA	Reg _{load}	_	30	100	mV
Output Voltage -7.0 Vdc \geq VI \geq -25 Vdc, 5.0 mA \leq IO \leq 350 mA	V _O	-4.75	-	-5.25	Vdc
Input Bias Current ($T_J = 25^{\circ}C$)	I _{IB}	-	4.3	8.0	mA
Input Bias Current Change -8.0 Vdc \geq VI \geq -25 Vdc, I_O = 350 mA 5.0 mA \leq I_O \leq 350 mA, VI = -10 V	ΔI _{IB}			0.4 0.4	mA
Output Noise Voltage, T_A = 25°C, 10 Hz \leq f \leq 100 kHz	V _n	-	40	-	μV
Ripple Rejection (f = 120 Hz)	RR	54	66	-	dB
Dropout Voltage $I_0 = 500 \text{ mA}, \text{ T}_J = 25^{\circ}\text{C}$	V _I –V _O	_	1.1	_	Vdc
Average Temperature Coefficient of Output Voltage I_{O} = 5.0 mA, 0°C \leq T_{J} \leq 125°C	$\Delta V_O / \Delta T$	_	0.2	_	mV/°C

1. Load and line regulation are specified at constant temperature. Change in V_O due to heating effects must be taken into account separately. Pulse testing with low duty cycle is used. 2. $B = T_{low}$ to T_{high} , $-40^{\circ}C < T_{J} < 125^{\circ}C C = T_{low}$ to T_{high} , $0^{\circ}C < T_{J} < 125^{\circ}C$.

MC79M08B, C

ELECTRICAL CHARACTERISTICS (VI = -10 V, IO = 350 mA, Tlow to Thigh (Note 4), unless otherwise noted.)

Characteristic	Symbol	Min	Тур	Max	Unit
Output Voltage ($T_J = 25^{\circ}C$)	Vo	-7.7	-8.0	-8.3	Vdc
$ Line \ Regulation, \ T_J = 25^\circ C \ (Note \ 3) \\ -7.0 \ Vdc \geq V_I \geq -25 \ Vdc \\ -8.0 \ Vdc \geq V_I \geq -18 \ Vdc \\ $	Reg _{line}		5.0 3.0	80 50	mV
Load Regulation, T _J = 25°C (Note 3) 5.0 mA \leq I _O \leq 500 mA	Reg _{load}	_	30	100	mV
Output Voltage -7.0 Vdc \geq VI \geq -25 Vdc, 5.0 mA \leq IO \leq 350 mA	Vo	-7.6	-8.0	-8.4	Vdc
Input Bias Current ($T_J = 25^{\circ}C$)	I _{IB}	-	-	8.0	mA
Input Bias Current Change -8.0 Vdc \geq V_I \geq -25 Vdc, I_O = 350 mA 5.0 mA \leq I_O \leq 350 mA, V_I = -10 V	ΔI _{IB}			0.4 0.4	mA
Output Noise Voltage, T_A = 25°C, 10 Hz \leq f \leq 100 kHz	V _n	-	60	-	μV
Ripple Rejection (f = 120 Hz)	RR	54	63	-	dB
Dropout Voltage $I_0 = 500 \text{ mA}, T_J = 25^{\circ}\text{C}$	V _I –V _O	_	1.1	_	Vdc
Average Temperature Coefficient of Output Voltage I_O = 5.0 mA, 0°C \leq T_J \leq 125°C	ΔV _O /ΔT	_	0.4	_	mV/°C

3. Load and line regulation are specified at constant temperature. Change in V_O due to heating effects must be taken into account separately. 2. Evaluation are regulation are specified at CC Pulse testing with low duty cycle is used.
4. B = T_{low} to T_{high}, -40°C < T_J < 125°C C = T_{low} to T_{high}, 0°C < T_J < 125°C

MC79M12B, C **ELECTRICAL CHARACTERISTICS** ($V_I = -19 V$, $I_O = 350 mA$, T_{low} to T_{high} (Note 6), unless otherwise noted.)

Characteristic	Symbol	Min	Тур	Max	Unit
Output Voltage ($T_J = 25^{\circ}C$)	V _O	-11.5	-12	-12.5	Vdc
Line Regulation, $T_J = 25^{\circ}C$ (Note 5) -14.5 Vdc $\ge V_I \ge -30$ Vdc -15 Vdc $\ge V_I \ge -25$ Vdc	Reg _{line}		5.0 3.0	80 50	mV
Load Regulation, $T_J = 25^{\circ}C$ (Note 5) 5.0 mA $\leq I_O \leq$ 500 mA	Reg _{load}	-	30	240	mV
Output Voltage -14.5 Vdc \geq V _I \geq -30 Vdc, 5.0 mA \leq I _O \leq 350 mA	Vo	-11.4	-	-12.6	Vdc
Input Bias Current ($T_J = 25^{\circ}C$)	I _{IB}	-	4.4	8.0	mA
Input Bias Current Change -14.5 Vdc \geq V _I \geq -30 Vdc, I _O = 350 mA 5.0 mA \leq I _O \leq 350 mA, V _I = -19 V	Δl _{IB}			0.4 0.4	mA
Output Noise Voltage, T_A = 25°C, 10 Hz \leq f \leq 100 kHz	V _n	-	75	-	μV
Ripple Rejection (f = 120 Hz)	RR	54	60	_	dB
Dropout Voltage $I_0 = 500 \text{ mA}, \text{ T}_J = 25^{\circ}\text{C}$	V _I –V _O	_	1.1	-	Vdc
Average Temperature Coefficient of Output Voltage I_{O} = 5.0 mA, 0°C \leq T_{J} \leq 125°C	$\Delta V_{O} / \Delta T$	_	-0.8	_	mV/°C

5. Load and line regulation are specified at constant temperature. Change in V_O due to heating effects must be taken into account separately. Pulse testing with low duty cycle is used.
6. B = T_{low} to T_{high}, -40°C < T_J < 125°C C = T_{low} to T_{high}, 0°C < T_J < 125°C

MC79M15B, C

ELECTRICAL CHARACTERISTICS ($V_I = -23 V$, $I_O = 350 mA$, T_{low} to T_{high} (Note 8), unless otherwise noted.)

Characteristic	Symbol	Min	Тур	Max	Unit
Output Voltage ($T_J = 25^{\circ}C$)	Vo	-14.4	-15	-15.6	Vdc
$ \begin{array}{l} \mbox{Line Regulation, } T_J = 25^\circ C \ (\mbox{Note 7}) \\ -17.5 \ \mbox{Vdc} \geq V_I \geq -30 \ \mbox{Vdc} \\ -18 \ \mbox{Vdc} \geq V_I \geq -28 \ \mbox{Vdc} \end{array} $	Reg _{line}		5.0 3.0	80 50	mV
Load Regulation, T _J = 25°C (Note 7) 5.0 mA \leq I _O \leq 500 mA	Reg _{load}	_	30	240	mV
Output Voltage -17.5 Vdc \geq V _I \geq -30 Vdc, 5.0 mA \leq I _O \leq 350 mA	Vo	-14.25	-	-15.75	Vdc
Input Bias Current ($T_J = 25^{\circ}C$)	I _{IB}	-	4.4	8.0	mA
Input Bias Current Change $-17.5 \text{ Vdc} \ge V_I \ge -30 \text{ Vdc}, I_O = 350 \text{ mA}$ $5.0 \text{ mA} \le I_O \le 350 \text{ mA}, V_I = -23 \text{ V}$	Δl _{IB}			0.4 0.4	mA
Output Noise Voltage, T_A = 25°C, 10 Hz \leq f \leq 100 kHz	V _n	-	90	-	μV
Ripple Rejection (f = 120 Hz)	RR	54	60	-	dB
Dropout Voltage $I_0 = 500 \text{ mA}, \text{ T}_J = 25^{\circ}\text{C}$	V _I –V _O	_	1.1	_	Vdc
Average Temperature Coefficient of Output Voltage I_O = 5.0 mA, 0°C \leq T_J \leq 125°C	$\Delta V_O / \Delta T$	_	-1.0	-	mV/°C

 Load and line regulation are specified at constant temperature. Change in V_O due to heating effects must be taken into account separately. Pulse testing with low duty cycle is used.

Pulse testing with low duty cycle is used.
B = T_{low} to T_{high}, -40°C < T_J < 125°C C = T_{low} to T_{high}, 0°C < T_J < 125°C





ORDERING INFORMATION

Device	Output Voltage Tolerance	Operating Temperature Range	Package	Shipping [†]
MC79M05BDT			DPAK	75 Units / Rail
MC79M05BDTG			DPAK (Pb–Free)	75 Units / Rail
MC79M05BDTRK			DPAK	2500 Units / Reel
MC79M05BDTRKG		$T_J = -40^{\circ}C$ to $+125^{\circ}C$	DPAK (Pb–Free)	2500 Units / Reel
MC79M05BT			TO-220	50 Units / Rail
MC79M05BTG			TO–220 (Pb–Free)	50 Units / Rail
MC79M05CDT			DPAK	75 Units / Rail
MC79M05CDTG			DPAK (Pb–Free)	75 Units / Rail
MC79M05CDTRK			DPAK	2500 Units / Reel
MC79M05CDTRKG		$T_J = 0^{\circ}C$ to +125°C	DPAK (Pb–Free)	2500 Units / Reel
MC79M05CT			TO-220	50 Units / Rail
MC79M05CTG			TO–220 (Pb–Free)	50 Units / Rail
MC79M08BDT			DPAK	75 Units / Rail
MC79M08BDTRK			DPAK	2500 Units / Reel
MC79M08BDTRKG		$T_{J} = -40^{\circ}C \text{ to } +125^{\circ}C$	DPAK (Pb–Free)	2500 Units / Reel
MC79M08BT			TO-220	50 Units / Rail
MC79M08CDT	4.0%		DPAK	75 Units / Rail
MC79M08CDTG	1.070		DPAK (Pb–Free)	75 Units / Rail
MC79M08CDTRK		T _J = 0°C to +125°C	DPAK	2500 Units / Reel
MC79M08CDTRKG			DPAK (Pb–Free)	2500 Units / Reel
MC79M08CT			TO-220	50 Units / Rail
MC79M12BDT			DPAK	75 Units / Rail
MC79M12BDTG			DPAK (Pb–Free)	75 Units / Rail
MC79M12BDTRK			DPAK	2500 Units / Reel
MC79M12BDTRKG		$T_J = -40^{\circ}C$ to $+125^{\circ}C$	DPAK (Pb–Free)	2500 Units / Reel
MC79M12BT			TO-220	50 Units / Rail
MC79M12BTG			TO–220 (Pb–Free)	50 Units / Rail
MC79M12CDT			DPAK	75 Units / Rail
MC79M12CDTG			DPAK (Pb–Free)	75 Units / Rail
MC79M12CDTRK			DPAK	2500 Units / Reel
MC79M12CDTRKG		$T_J = 0^{\circ}C$ to +125°C	DPAK (Pb–Free)	2500 Units / Reel
MC79M12CT			TO-220	50 Units / Rail
MC79M12CTG			TO–220 (Pb–Free)	50 Units / Rail

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

ORDERING INFORMATION

Device	Output Voltage Tolerance	Operating Temperature Range	Package	Shipping†
MC79M15BDT			DPAK	75 Units / Rail
MC79M15BDTG			DPAK (Pb–Free)	75 Units / Rail
MC79M15BDTRK	1		DPAK	2500 Units / Reel
MC79M15BDTRKG		$T_J = -40^{\circ}C$ to $+125^{\circ}C$	DPAK (Pb–Free)	2500 Units / Reel
MC79M15BT	1		TO-220	50 Units / Rail
MC79M15BTG			TO-220 (Pb-Free)	50 Units / Rail
MC79M15CDT	4.0%		DPAK	75 Units / Rail
MC79M15CDTG			DPAK (Pb–Free)	75 Units / Rail
MC79M15CDTRK			DPAK	2500 Units / Reel
MC79M15CDTRKG		$T_J = 0^{\circ}C$ to +125°C	DPAK (Pb–Free)	2500 Units / Reel
MC79M15CT			TO-220	50 Units / Rail
MC79M15CTG			TO-220 (Pb-Free)	50 Units / Rail

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

PACKAGE DIMENSIONS

TO-220 PLASTIC PACKAGE T SUFFIX CASE 221A-09 **ISSUE AA**



NOTES: 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. 2. CONTROLLING DIMENSION: INCH. 3. DIMENSION Z DEFINES A ZONE WHERE ALL BODY AND LEAD IRREGULARITIES ARE ALLOWED.

	INC	HES	MILLIN	IETERS	
DIM	MIN	MAX	MIN	MAX	
Α	0.570	0.620	14.48	15.75	
В	0.380	0.405	9.66	10.28	
С	0.160	0.190	4.07	4.82	
D	0.025	0.035	0.64	0.88	
F	0.142	0.147	3.61	3.73	
G	0.095	0.105	2.42	2.66	
Н	0.110	0.155	2.80	3.93	
J	0.018	0.025	0.46	0.64	
К	0.500	0.562	12.70	14.27	
L	0.045	0.060	1.15	1.52	
Ν	0.190	0.210	4.83	5.33	
Q	0.100	0.120	2.54	3.04	
R	0.080	0.110	2.04	2.79	
S	0.045	0.055	1.15	1.39	
Т	0.235	0.255	5.97	6.47	
U	0.000	0.050	0.00	1.27	
V	0.045		1.15		
Z		0.080		2.04	

DPAK-3 DT SUFFIX CASE 369C-01 ISSUE O



NOTES:

1. DIMENSIONING AND TOLERANCING DER ANSI X14 5M 1982

PER ANSI Y14.5M, 1982. 2. CONTROLLING DIMENSION: INCH.

	INC	HES	MILLIN	IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.235	0.245	5.97	6.22
В	0.250	0.265	6.35	6.73
С	0.086	0.094	2.19	2.38
D	0.027	0.035	0.69	0.88
E	0.018	0.023	0.46	0.58
F	0.037	0.045	0.94	1.14
G	0.180	BSC	4.58	BSC
н	0.034	0.040	0.87	1.01
J	0.018	0.023	0.46	0.58
ĸ	0.102	0.114	2.60	2.89
L	0.090	0.090 BSC		BSC
R	0.180	0.215	4.57	5.45
S	0.025	0.040	0.63	1.01
U	0.020		0.51	
v	0.035	0.050	0.89	1.27
Z	0.155		3.93	

SOLDERING FOOTPRINT*



*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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