

MTP3055V

N-Channel Enhancement Mode Field Effect Transistor

General Description

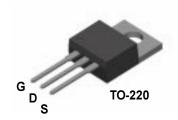
This N-Channel MOSFET has been designed specifically for low voltage, high speed switching applications i.e. power supplies and power motor controls.

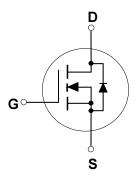
These MOSFETs feature faster switching and lower gate charge than other MOSFETs with comparable $R_{\scriptscriptstyle \mathrm{DS(ON)}}$ specifications.

The result is a MOSFET that is easy and safer to drive (even at very high frequencies).

Features

- 12 A, 60 V. $R_{DS(ON)} = 0.150 \Omega @ V_{GS} = 10 V$
- Critical DC electrical parameters specified at elevated temperature.
- Rugged internal source-drain diode can eliminate the need for an external Zener diode transient suppressor.
- 175°C maximum junction temperature rating.





Absolute Maximum Ratings T_C = 25°C unless otherwise noted

	Absolute Maximum Nauriys 1,2-20 o uniess outerwise noted				
Symbol	Parameter	Ratings	Units		
V _{DSS}	Drain-Source Voltage	60	V		
V _{GSS}	Gate-Source Voltage	<u>+</u> 20	V		
I _D	Drain Current - Continuous	12	А		
	- Pulsed	37			
P_D	Total Power Dissipation @ T _C = 25∘C	48	W		
	Derate above 25∘C	0.32	W/∘C		
T_J,T_STG	Operating and Storage Junction Temperature Range	-65 to +175	∘C		
Therma	l Characteristics				
R ₀ Jc	Thermal Resistance, Junction-to- Case	3.13	∘C/W		
$R_{\theta^{\mathrm{JA}}}$	Thermal Resistance, Junction-to- Ambient (Note 1)	62.5	∘C/W		

Package Outlines and Ordering Information

Device Marking	Device	Package Information	Quantity
MTP3055V	MTP3055V	Rails/Tubes	45 units
MTP3055V	MH P3055V	Ralls/Tubes	45

^{*} Die and manufacturing source subject to change without prior notification.

Symbol	Parameter	Test Conditions	Min	qyT	Max	Units
DRAIN-S	OURCE AVALANCHE RATI	NGS (Note 2)				
\mathbf{w}_{DSS}	Single Pulse Drain-Source Avalanche Energy	$V_{DD} = 25 \text{ V}, \mid_{D} = 12 \text{ A}$			72	mЈ
I _{AR}	Maximum Drain-Source Avalanche	e Current			12	Α
Off Chara	ctorietice		•			
BV _{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, \mid_{D} = 250 \mu\text{A}$	60			V
<u>Δ</u> BVDSS ΔΤ _J	Breakdown Voltage Temperature Coefficient	I_D = 250 μ A, Referenced to 25°C		63		mV/∘C
DSS	Zero Gate Voltage Drain Current	V _{DS} = 60 V, V _{GS} = 0 V			10	μΑ
DSS	Zero Gate Voltage Drain Current	V _{DS} = 60 V, V _{GS} = 0 V, T _J = 150∘C			100	μA
GSSF	Gate-Body Leakage Current, Forward	V _{GS} = 20 V, V _{DS} = 0 V			100	nA
GSSR	Gate-Body Leakage Current, Reverse	V _{GS} = -20 V, V _{DS} = 0 V			-100	nA
On Chara	cteristics (Note 2)					
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2	3	4	V
$\Delta V_{GS(th)} \over \Delta T_J$	Gate Threshold Voltage Temperature Coefficient	I _D = 250 μA, Referenced to 25°C		-5		mV/∘0
R _{DS(on)}	Static Drain-Source On-Resistance	V _{GS} = 10 V,I _D = 6 A,			0.150	Ω
$V_{DS(on)}$	Drain Source On-Voltage	I _D = 12 A, V _{GS} = 10 V			2.2	V
g FS	Forward Transconductance	$V_{DS} = 7 V, I_{D} = 6 A$	4.0			S
Dvnamic (Characteristics					
C _{iss}	Input Capacitance	V _{DS} = 25 V, V _{GS} = 0 V,			500	pF
Coss	Output Capacitance	f = 1.0 MHz			180	pF
C _{rss}	Reverse Transfer Capacitance				50	pF
Switching	Characteristics (Note 2)					
t _{d(on)}	Turn-On Delay Time	V _{DD} = 30 V, I _D = 12 A,			16	ns
t _r	Turn-On Rise Time	$V_{GS} = 10 \text{ V}, R_{GEN} = 9.1 \Omega$			38	ns
t _{d(off)}	Turn-Off Delay Time				80	ns
t _f	Turn-Off Fall Time				45	ns
Q _g	Total Gate Charge	V _{DS} = 48 V, I _D = 12 A, V _{GS} = 10 V			17	nC
Q _{gs}	Gate-Source Charge			2.3		nC
Q_{gd}	Gate-Drain Charge			2.6		nC
Drain-Sou	rce Diode Characteristics a	and Maximum Ratings				
اری ا _ی	Maximum Continuous Drain-Source	•			12	Α
Ism	Maximum Pulsed Drain-Source Di				37	A
V _{SD}	Drain-Source Diode Forward Voltage	$V_{GS} = 0 \text{ V}, I_S = 12 \text{ A}$ (Note 2)			1.6	V

Notes:

^{1.} R_{8JA} is the sum of the juntion-to-case and case-to-ambient thermal resistance. 2. Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2.0%

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