# **High Voltage Darlington Transistors** NPN Silicon

### MAXIMUM RATINGS

Rating	Symbol	BC372	BC373	Unit	
Collector-Emitter Voltage	VCES	100	80	Vdc	
Collector-Base Voltage	VCBO	100	80	Vdc	
Emitter-Base Voltage	VEBO	12		Vdc	
Collector Current — Continuous	IC	1.0		Adc	
Total Device Dissipation @ T <sub>A</sub> = 25°C Derate above 25°C	PD	625 5.0		mW mW/°C	
Total Device Dissipation @ T <sub>C</sub> = 25°C Derate above 25°C	PD	1.5 12		Watt mW/°C	
Operating and Storage Junction Temperature Range	TJ, Tstg	-55 to +150		°C	



**BC372** 

**BC373** 

BASE 2

COLLECTOR 3

EMITTER 1

### THERMAL CHARACTERISTICS

Characteristic	Symbol	Мах	Unit
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	200	°C/W
Thermal Resistance, Junction to Case	$R_{\theta}JC$	83.3	°C/W

### ELECTRICAL CHARACTERISTICS (T<sub>A</sub> = 25°C unless otherwise noted)

Characteris	tic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS		·				
Collector–Emitter Breakdown Voltage <sup>(1)</sup> (I <sub>C</sub> = 100 $\mu$ Adc, I <sub>B</sub> = 0)	BC372 BC373	V(BR)CES	100 80			Vdc
Collector–Base Breakdown Voltage ( $I_C = 100 \ \mu Adc$ , $I_E = 0$ )	BC372 BC373	V(BR)CBO	100 80			Vdc
Emitter–Base Breakdown Voltage (IE = 10 $\mu$ Adc, IC = 0)		V <sub>(BR)EBO</sub>	12	—	—	Vdc
Collector Cutoff Current ( $V_{CB} = 80 \text{ Vdc}, I_E = 0$ ) ( $V_{CB} = 60 \text{ Vdc}, I_E = 0$ )	BC372 BC373	ІСВО			100 100	nAdc
Emitter Cutoff Current ( $V_{EB} = 10 \text{ V}, I_{C} = 0$ )		IEBO	_	—	100	nAdc

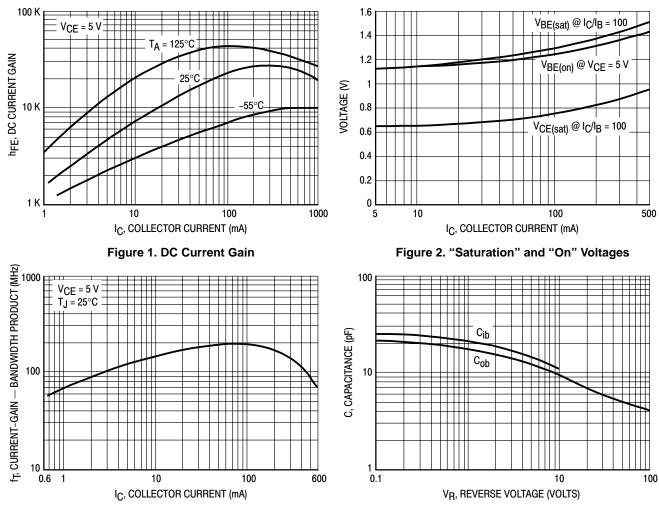
1. Pulse Test: Pulse Width = 300  $\mu$ s, Duty Cycle 2.0%.

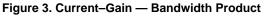
# BC372 BC373

### ELECTRICAL CHARACTERISTICS (T<sub>A</sub> = 25°C unless otherwise noted) (Continued)

Oh ann a taola tha	0		<b>T</b>		11
Characteristic	Symbol	Min	Тур	Max	Unit
ON CHARACTERISTICS(1)					
DC Current Gain (I <sub>C</sub> = 250 mAdc, V <sub>CE</sub> = 5.0 Vdc) (I <sub>C</sub> = 100 mAdc, V <sub>CE</sub> = 5.0 Vdc)	hFE	8.0 10		 160	К
Collector–Emitter Saturation Voltage ( $I_C = 250 \text{ mAdc}, I_B = 0.25 \text{ mAdc}$ )	V <sub>CE(sat)</sub>	_	1.0	1.1	Vdc
Base–Emitter Saturation Voltage ( $I_C = 250 \text{ mAdc}$ , $I_B = 0.25 \text{ mAdc}$ )	V <sub>BE(sat)</sub>	_	1.4	2.0	Vdc
DYNAMIC CHARACTERISTICS					
Current–Gain Bandwidth Product (I <sub>C</sub> = 100 mAdc, V <sub>CE</sub> = 5.0 Vdc, f = 100 MHz)	fŢ	100	200	—	MHz
Output Capacitance (V <sub>CB</sub> = 10 Vdc, I <sub>E</sub> = 0, f = 1.0 MHz)	C <sub>ob</sub>	_	10	25	pF
Noise Figure (I <sub>C</sub> = 1.0 mAdc, V <sub>CE</sub> = 5.0 Vdc, R <sub>g</sub> = 100 k ohm, f = 1.0 kHz)	NF	_	2.0	—	dB

1. Pulse Test: Pulse Width =  $300 \ \mu s$ , Duty Cycle 2.0%.



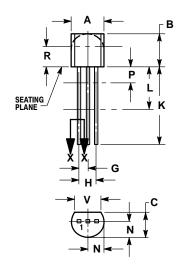


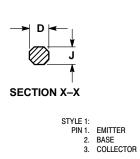


# BC372 BC373

## PACKAGE DIMENSIONS

TO-92 (TO-226) CASE 29-11 ISSUE AL





NOTES: 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. 2. CONTROLLING DIMENSION: INCH. 3. CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED. 4. LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

	INCHES		MILLIN	ETERS	
DIM	MIN	MAX	MIN	MAX	
Α	0.175	0.205	4.45	5.20	
В	0.170	0.210	4.32	5.33	
C	0.125	0.165	3.18	4.19	
D	0.016	0.021	0.407	0.533	
G	0.045	0.055	1.15	1.39	
Н	0.095	0.105	2.42	2.66	
J	0.015	0.020	0.39	0.50	
K	0.500		12.70		
L	0.250		6.35		
Ν	0.080	0.105	2.04	2.66	
Ρ		0.100		2.54	
R	0.115		2.93		
۷	0.135		3.43		

### BC372 BC373

**ON Semiconductor** and without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer.

#### PUBLICATION ORDERING INFORMATION

#### Literature Fulfillment:

Literature Distribution Center for ON Semiconductor P.O. Box 5163, Denver, Colorado 80217 USA

Phone: 303–675–2175 or 800–344–3860 Toll Free USA/Canada Fax: 303–675–2176 or 800–344–3867 Toll Free USA/Canada Email: ONlit@hibbertco.com

N. American Technical Support: 800-282-9855 Toll Free USA/Canada

JAPAN: ON Semiconductor, Japan Customer Focus Center 4–32–1 Nishi–Gotanda, Shinagawa–ku, Tokyo, Japan 141–0031 Phone: 81–3–5740–2700 Email: r14525@onsemi.com

ON Semiconductor Website: http://onsemi.com

For additional information, please contact your local Sales Representative.