

# DS1995 16-kbit Memory <u>i</u>Button<sup>™</sup>

#### www.dalsemi.com

#### SPECIAL FEATURES

- 16384 bits of read/write nonvolatile memory
- 256-bit scratchpad ensures integrity of data transfer
- Overdrive mode boosts communication to 142 kbits per second
- Memory partitioned into 256-bit pages for packetizing data
- Data integrity assured with strict read/write protocols
- Operating temperature range from -40°C to +70°C
- Over 10 years of data retention

### **COMMON** <u>i</u>Button FEATURES

- Unique, factory-lasered and tested 64-bit registration number (8-bit family code + 48bit serial number + 8-bit CRC tester) assures absolute traceability because no two parts are alike
- Multidrop controller for MicroLAN<sup>TM</sup>
- Digital identification and information by momentary contact
- Chip-based data carrier compactly stores information
- Data can be accessed while affixed to object
- Economically communicates to bus master with a single digital signal at 16.3k bits per second
- Standard 16 mm diameter and 1-Wire protocol ensure compatibility with <u>i</u>Button family
- Button shape is self-aligning with cupshaped probes
- Durable stainless steel case engraved with registration number withstands harsh environments
- Easily affixed with self-stick adhesive backing, latched by its flange, or locked with a ring pressed onto its rim
- Presence detector acknowledges when reader first applies voltage

 Meets UL#913 (4th Edit.); Intrinsically Safe Apparatus, approved under Entity Concept for use in Class I, Division 1, Group A, B, C and D Locations (application pending)

# F5 MICROCAN<sup>™</sup>



All dimensions are shown in millimeters.

### **ORDERING INFORMATION**

DS1995L–F5 F5 MicroCan

# EXAMPLES OF ACCESSORIES

DS9096P	Sel
DS9101	Mu
DS9093RA	Мо
DS9093F	Sna
DS9092	<u>i</u> Bu

Self-Stick Adhesive Pad Multi-Purpose Clip Mounting Lock Ring Snap-In Fob <u>i</u>Button Probe

# **iButton DESCRIPTION**

The DS1995 Memory <u>i</u>Button operates nearly identically to the DS1996. The main differences are: 16 kbits of memory organized as 64 pages of 32 bytes and a family code of 0A hexadecimal. For further details please refer to the DS1996 data sheet.