

# Integrated Silicon Pressure Sensor Manifold Absolute Pressure Sensor On-Chip Signal Conditioned, Temperature Compensated and Calibrated

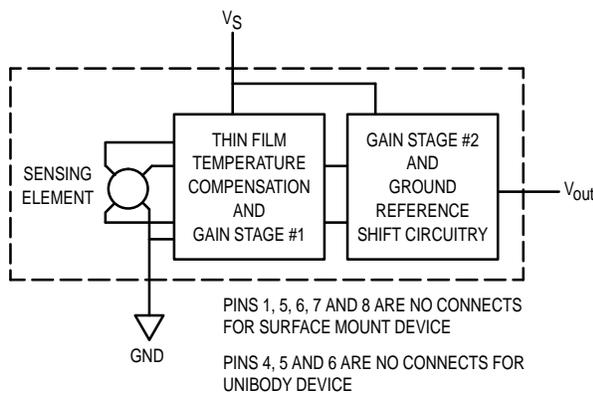
The Motorola MPX4100A/MPXS4100A/MPXT4100A series Manifold Absolute Pressure (MAP) sensor for engine control is designed to sense absolute air pressure within the intake manifold. This measurement can be used to compute the amount of fuel required for each cylinder. The small form factor and high reliability of on-chip integration makes the Motorola MAP sensor a logical and economical choice for automotive system designers.

## Features

- 1.8% Maximum Error Over 0° to 85°C
- Specifically Designed for Intake Manifold Absolute Pressure Sensing in Engine Control Systems
- Ideally Suited for Microprocessor Interfacing
- Temperature Compensated Over -40°C to +125°C
- Durable Epoxy Unibody Element or Surface Mount Package
- Also Ideal for Non-Automotive Applications

## Application Examples

- Manifold Sensing for Automotive Systems



**Figure 1. Fully Integrated Pressure Sensor Schematic**

**MPX4100A  
MPXS4100A  
MPXT4100A  
MPXA4100A  
SERIES**

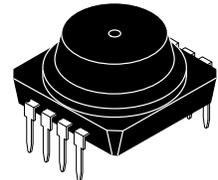
**INTEGRATED  
PRESSURE SENSOR  
20 to 105 kPa (2.9 to 15.2 psi)  
0.3 to 4.9 V Output**

### SURFACE MOUNT PACKAGE



**MPXS4100A  
CASE 471-01, STYLE 1**

### TOP PISTON FIT PACKAGE



**MPXT4100A  
BASIC ELEMENT  
CASE 473A-01, STYLE 2**

### PIN NUMBER

1	N/C	3	Gnd	5	N/C	7	N/C
2	V <sub>S</sub>	4	V <sub>out</sub>	6	N/C	8	N/C

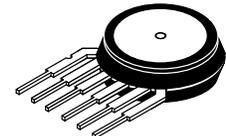
NOTE: Pins 1, 5, 6, 7, and 8 are not device connections. Do not connect to external circuitry or ground. Pin 1 is noted by the notch in the Lead.

### SMALL OUTLINE PACKAGE



**MPXA4100A  
CASE 482-01**

### UNIBODY PACKAGE



**MPX4100A  
BASIC CHIP CARRIER ELEMENT  
CASE 867-08, STYLE 1**

### PIN NUMBER

1	N/C	5	N/C
2	V <sub>S</sub>	6	N/C
3	Gnd	7	N/C
4	V <sub>out</sub>	8	N/C

### PIN NUMBER

1	V <sub>out</sub>	4	N/C
2	Gnd	5	N/C
3	V <sub>S</sub>	6	N/C

NOTE: Pins 1, 5, 6, 7, and 8 are not device connections. Do not connect to external circuitry or ground. Pin 1 is noted by the notch in the Lead.

NOTE: Pins 4, 5, and 6 are internal device connections. Do not connect to external circuitry or ground. Pin 1 is noted by the notch in the Lead.

The MPX4100A/MPXS4100A/MPXT4100A series piezoresistive transducer is a state-of-the-art, monolithic, signal conditioned, silicon pressure sensor. This sensor combines advanced micromachining techniques, thin film metallization, and bipolar semiconductor processing to provide an accurate, high level analog output signal that is proportional to applied pressure.

Figure 1 shows a block diagram of the internal circuitry integrated on a pressure sensor chip.

# MPX4100A MPXS4100A MPXT4100A MPXA4100A SERIES

## MAXIMUM RATINGS(1)

Parametric	Symbol	Value	Unit
Overpressure <sup>(2)</sup> (P1 > P2)	P <sub>max</sub>	400	kPa
Burst Pressure <sup>(2)</sup> (P1 > P2)	P <sub>burst</sub>	1000	kPa
Storage Temperature	T <sub>stg</sub>	-40 to +125	°C
Operating Temperature	T <sub>A</sub>	-40 to +125	°C

1. T<sub>C</sub> = 25°C unless otherwise noted.

2. Exposure beyond the specified limits may cause permanent damage or degradation to the device.

## OPERATING CHARACTERISTICS (V<sub>S</sub> = 5.1 Vdc, T<sub>A</sub> = 25°C unless otherwise noted, P1 > P2)

Characteristic	Symbol	Min	Typ	Max	Unit
Pressure Range <sup>(1)</sup>	POP	20	—	105	kPa
Supply Voltage <sup>(1)</sup>	V <sub>S</sub>	4.85	5.1	5.35	Vdc
Supply Current	I <sub>o</sub>	—	7.0	10	mAdc
Minimum Pressure Offset <sup>(3)</sup> @ V <sub>S</sub> = 5.1 Volts	V <sub>off</sub>	0.225	0.306	0.388	Vdc
Full Scale Output <sup>(4)</sup> @ V <sub>S</sub> = 5.1 Volts	V <sub>FSO</sub>	4.870	4.951	5.032	Vdc
Full Scale Span <sup>(5)</sup> @ V <sub>S</sub> = 5.1 Volts	V <sub>FSS</sub>	—	4.59	—	Vdc
Accuracy <sup>(6)</sup>	—	—	—	±1.8	%V <sub>FSS</sub>
Sensitivity	V/P	—	54	—	mV/kPa
Response Time <sup>(7)</sup>	t <sub>R</sub>	—	1.0	—	ms
Output Source Current at Full Scale Output	I <sub>o+</sub>	—	0.1	—	mAdc
Warm-Up Time <sup>(8)</sup>	—	—	20	—	ms
Offset Stability <sup>(9)</sup>	—	—	±0.5	—	%V <sub>FSS</sub>

Decoupling circuit shown in Figure 3 required to meet electrical specifications.

## MECHANICAL CHARACTERISTICS

Characteristic	Symbol	Min	Typ	Max	Unit
Weight, Basic Element (Case 473)	—	—	2.0	—	Grams
Weight, Basic Element (Case 867)	—	—	4.0	—	Grams
Common Mode Line Pressure <sup>(10)</sup>	—	—	—	690	kPa

### NOTES:

- 1.0 kPa (kiloPascal) equals 0.145 psi.
- Device is ratiometric within this specified excitation range.
- Offset (V<sub>off</sub>) is defined as the output voltage at the minimum rated pressure.
- Full Scale Output (V<sub>FSO</sub>) is defined as the output voltage at the maximum or full rated pressure.
- Full Scale Span (V<sub>FSS</sub>) is defined as the algebraic difference between the output voltage at full rated pressure and the output voltage at the minimum rated pressure.
- Accuracy (error budget) consists of the following:
  - Linearity: Output deviation from a straight line relationship with pressure over the specified pressure range.
  - Temperature Hysteresis: Output deviation at any temperature within the operating temperature range, after the temperature is cycled to and from the minimum or maximum operating temperature points, with zero differential pressure applied.
  - Pressure Hysteresis: Output deviation at any pressure within the specified range, when this pressure is cycled to and from the minimum or maximum rated pressure, at 25°C.
  - TcSpan: Output deviation over the temperature range of 0 to 85°C, relative to 25°C.
  - TcOffset: Output deviation with minimum rated pressure applied, over the temperature range of 0 to 85°C, relative to 25°C.
  - Variation from Nominal: The variation from nominal values, for Offset or Full Scale Span, as a percent of V<sub>FSS</sub>, at 25°C.
- Response Time is defined as the time for the incremental change in the output to go from 10% to 90% of its final value when subjected to a specified step change in pressure.
- Warm-up is defined as the time required for the product to meet the specified output voltage after the Pressure has been stabilized.
- Offset stability is the product's output deviation when subjected to 1000 hours of Pulsed Pressure, Temperature Cycling with Bias Test.
- Common mode pressures beyond specified may result in leakage at the case-to-lead interface.

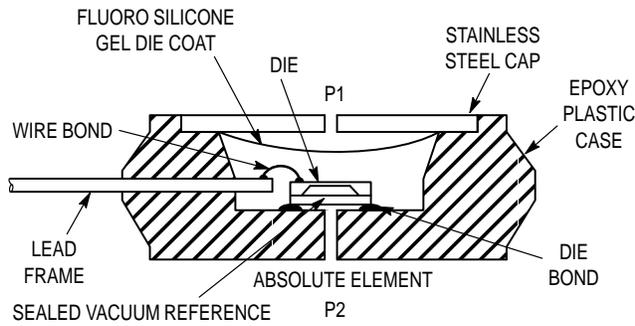


Figure 2. Cross Sectional Diagram (not to scale)

Figure 2 illustrates an absolute sensing chip in the basic chip carrier (Case 867).

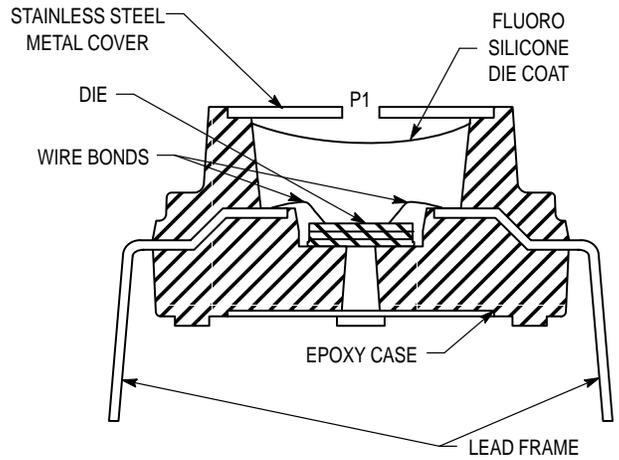


Figure 3. Cross-Sectional Diagram (not to scale)

Figure 3 illustrates an absolute sensing chip in surface mount carrier (Case 473).

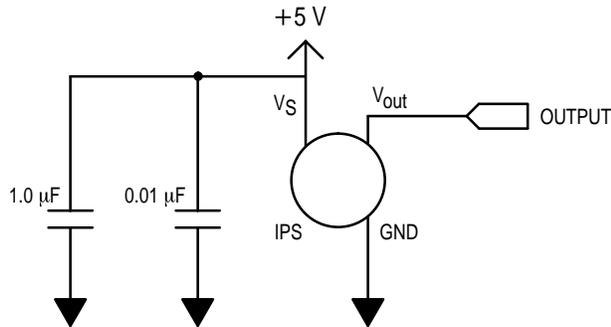


Figure 4. Recommended Power Supply Decoupling. For output filtering recommendations, please refer to Application Note AN1646.

Figure 5 shows the sensor output signal relative to pressure input. Typical, minimum, and maximum output curves are shown for operation over a temperature range of 0° to 85°C. (The output will saturate outside of the specified pressure range.)

A fluorosilicone gel isolates the die surface and wire bonds from the environment, while allowing the pressure signal to be transmitted to the sensor diaphragm. The

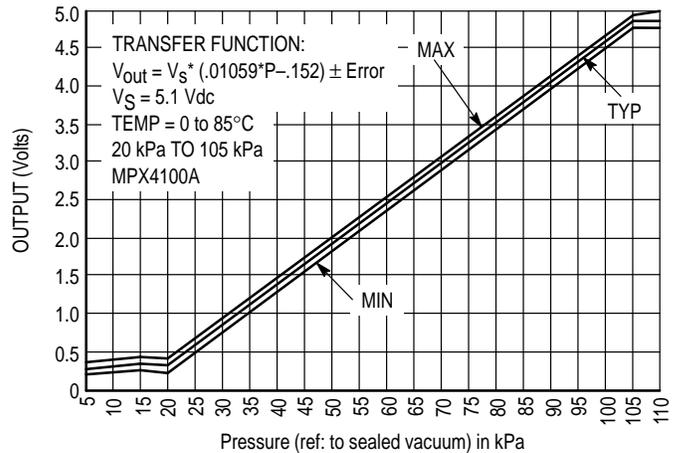


Figure 5. Output versus Absolute Pressure

MPX4100A/MPXS4100A/MPXT4100A series pressure sensor operating characteristics, and internal reliability and qualification tests are based on use of dry air as the pressure media. Media, other than dry air, may have adverse effects on sensor performance and long-term reliability. Contact the factory for information regarding media compatibility in your application.

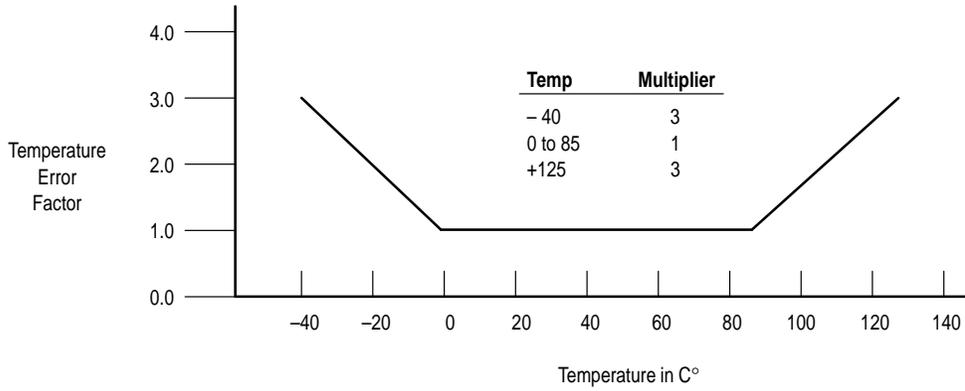
**MPX4100A MPXS4100A MPXT4100A MPXA4100A SERIES**

**Transfer Function (MPX4100A, MPXS4100A, MPXT4100A)**

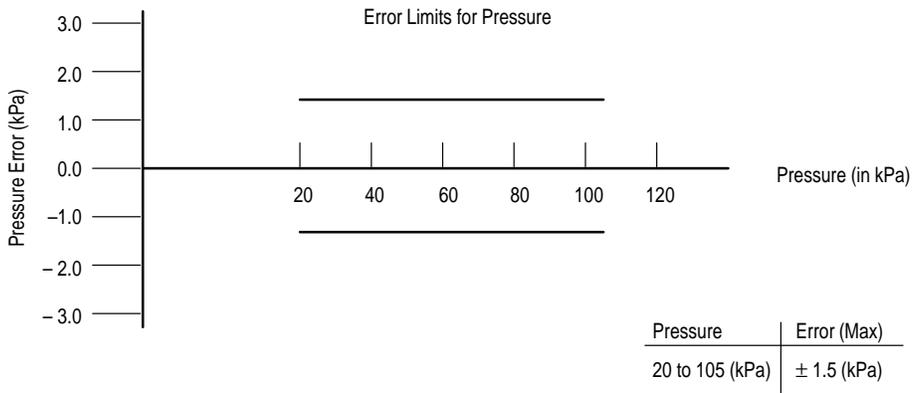
**Nominal Transfer Value:**  $V_{out} = V_S (P \times 0.01059 - 0.1518)$   
 $\pm (\text{Pressure Error} \times \text{Temp. Factor} \times 0.01059 \times V_S)$   
 $V_S = 5.1 \text{ V} \pm 0.25 \text{ Vdc}$

**Temperature Error Band**

**MPX4100A, MPXS4100A, MPXT4100A Series**



**Pressure Error Band**



**MPX4100A MPXS4100A MPXT4100A MPXA4100A SERIES**

**PRESSURE (P1)/VACUUM (P2) SIDE IDENTIFICATION TABLE**

Motorola designates the two sides of the pressure sensor as the Pressure (P1) side and the Vacuum (P2) side. The Pressure (P1) side is the side containing fluorosilicone gel which protects the die from harsh media. The Motorola MPX

pressure sensor is designed to operate with positive differential pressure applied,  $P1 > P2$ .

The Pressure (P1) side may be identified by using the table below:

Part Number	Case Type	Pressure (P1) Side Identifier
MPX4100A	867-08	Stainless Steel Cap
MPX4100AP	867B-04	Side with Port Marking
MPX4100AS	867E-03	Side with Port Attached
MPX4100ASX	867F-03	Side with Port Attached

**ORDERING INFORMATION — UNIBODY PACKAGE**

The MPX4100A series MAP silicon pressure sensors are available in the Basic Element, or with pressure port fittings that provide mounting ease and barbed hose connections.

Device Type	Options	Case Type	MPX Series	
			Order Number	Device Marking
Basic Element	Absolute, Element Only	867-08	MPX4100A	MPX4100A
Ported Elements	Absolute, Ported	867B-04	MPX4100AP	MPX4100AP
	Absolute, Stove Pipe Port	867E-03	MPX4100AS	MPX4100A
	Absolute, Axial Port	867F-03	MPX4100ASX	MPX4100A

**ORDERING INFORMATION — SURFACE MOUNT PACKAGE**

The MPXS4100A series BAP silicon pressure sensors are available shipped in sleeves or tape and reeled.

Device Type Options	Case No.	MPX Series Order No.	Marking
Sleeve Pack	471-01	MPXS4100A6U	MPXS4100A
Tape and Reel	471-01	MPXS4100A6T1	MPXS4100A
Sleeve Pack	471B-01	MPXS4100AC6U	MPXS4100A

Device	Reel Size	Tape Width	Quantity
MPXS4100A6T1	13" dia.	44 mm	250

**ORDERING INFORMATION — TOP PISTON FIT PACKAGE**

The MPXT4100A series MAP silicon pressure sensors are available to be shipped in rails.

Packing Options	Port Options	Leadform	Case No.	MPXT Series Order No.	Marking
Rails	Element	84°	473A-01	MPXT4100A7U	MPXT4100A

**ORDERING INFORMATION — SMALL OUTLINE PACKAGE**

Device	Case No.	Packing Options	Marking
MPXA4100A6U	482-01	Rails	MPXA4100A
MPXA4100A6T1	482-01	Tape and Reel	MPXA4100A

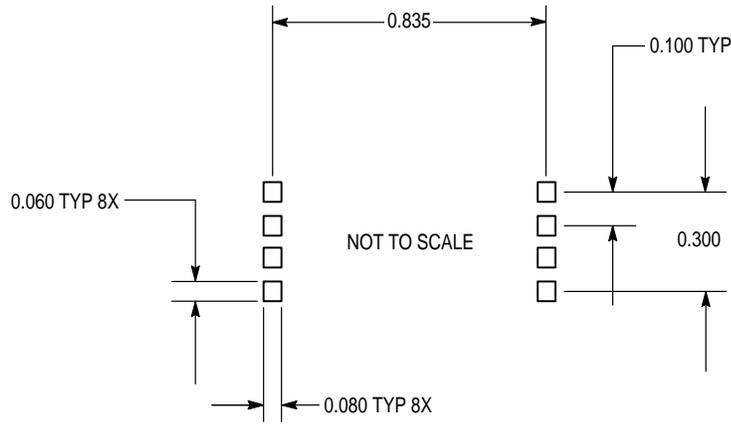
# MPX4100A MPXS4100A MPXT4100A MPXA4100A SERIES

## INFORMATION FOR USING THE SURFACE MOUNT PACKAGE

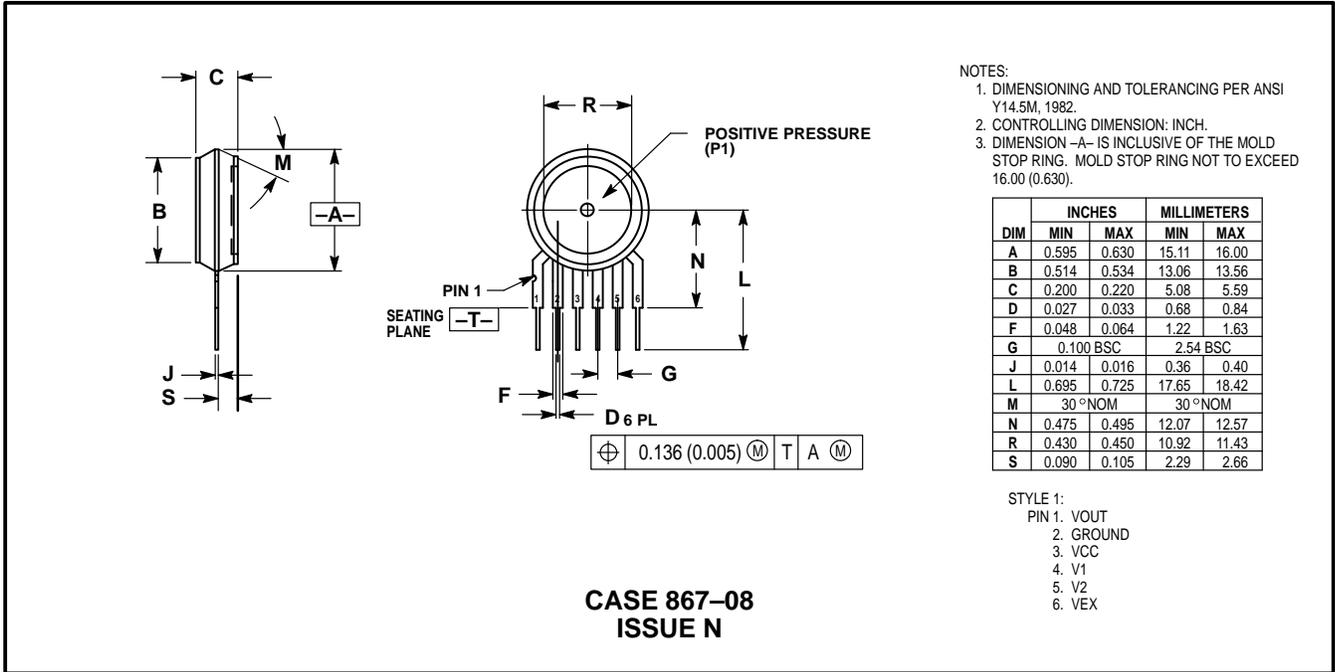
### MINIMUM RECOMMENDED FOOTPRINT FOR SURFACE MOUNTED APPLICATIONS

Surface mount board layout is a critical portion of the total design. The footprint for the semiconductor packages must be the correct size to ensure proper solder connection inter-

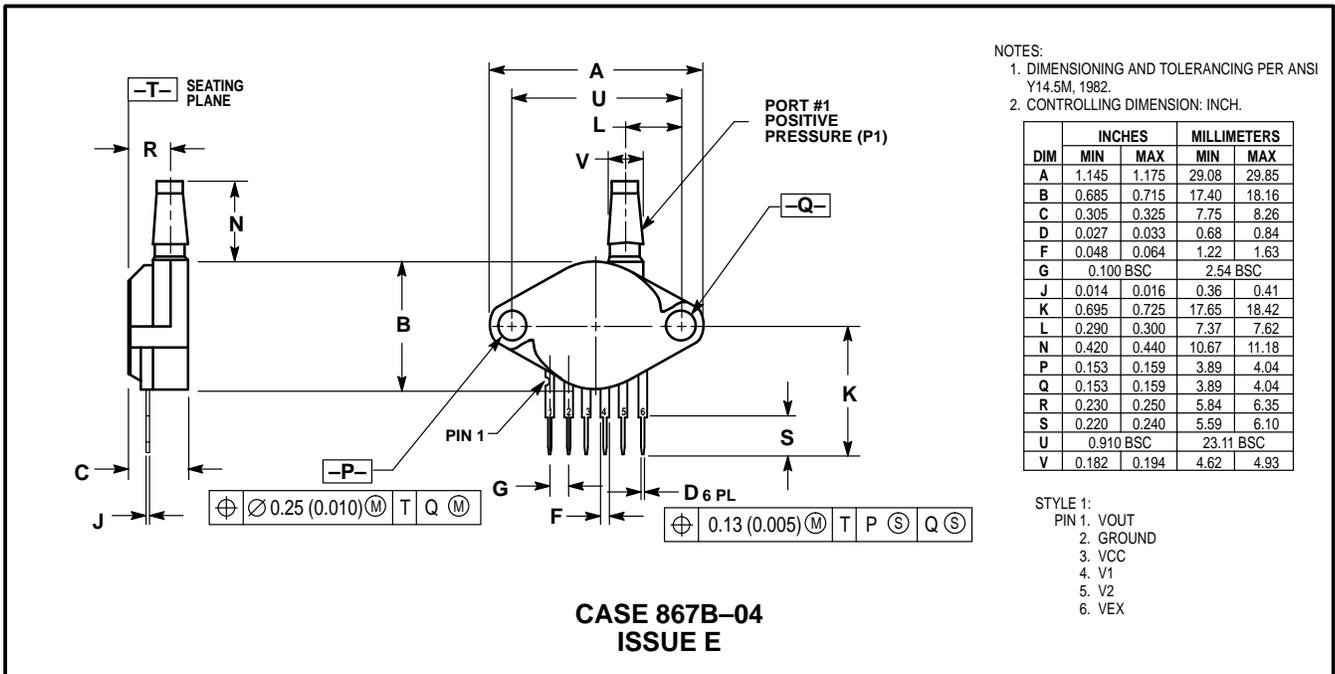
face between the board and the package. With the correct pad geometry, the packages will self align when subjected to a solder reflow process.



**MPX4100A MPXS4100A MPXT4100A MPXA4100A SERIES**  
**UNIBODY PACKAGE DIMENSIONS**



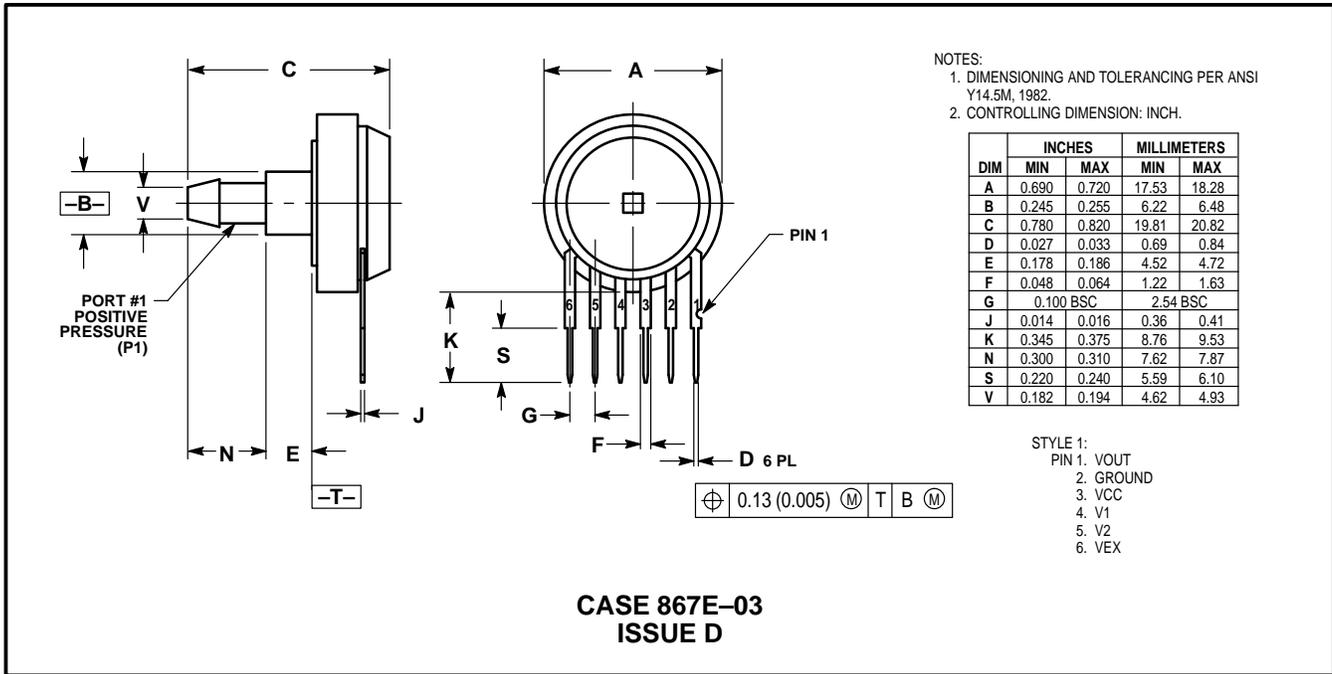
**BASIC ELEMENT (A, D)**



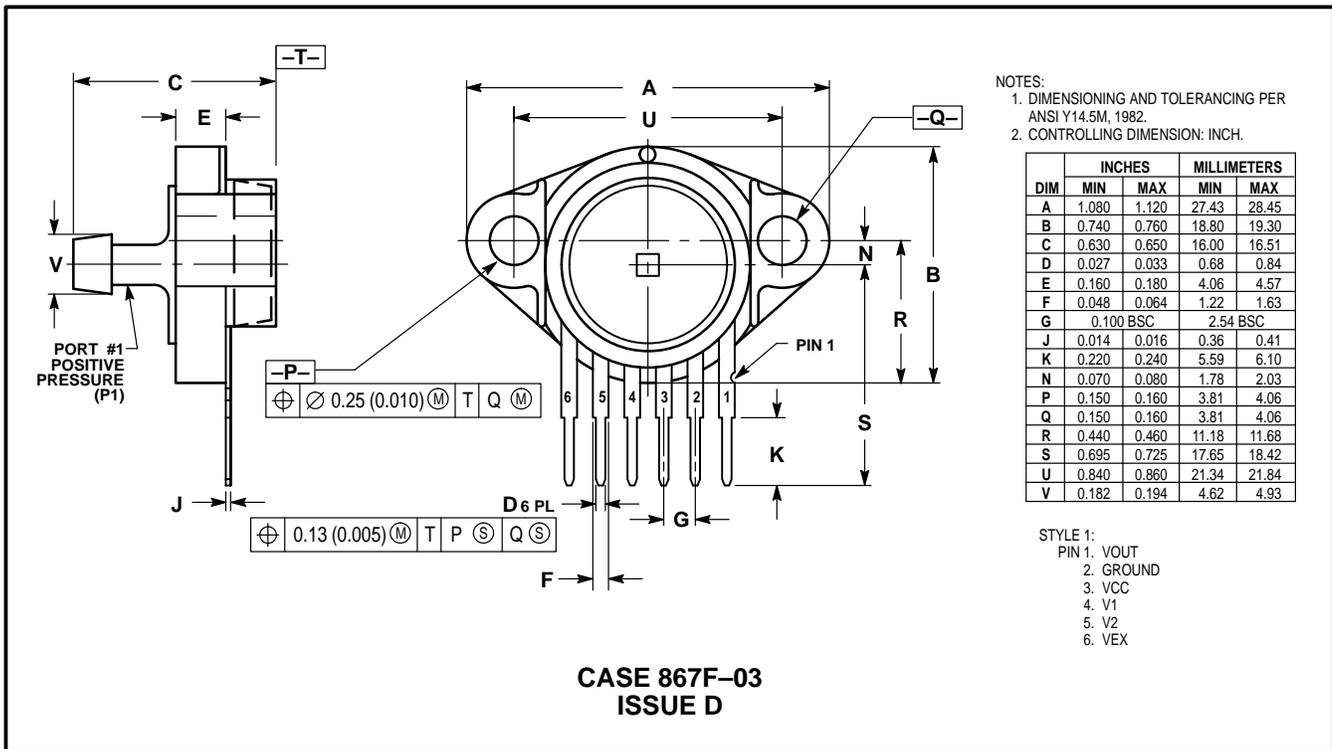
**PRESSURE SIDE PORTED (AP, GP)**

**MPX4100A MPXS4100A MPXT4100A MPXA4100A SERIES**

**UNIBODY PACKAGE DIMENSIONS—CONTINUED**

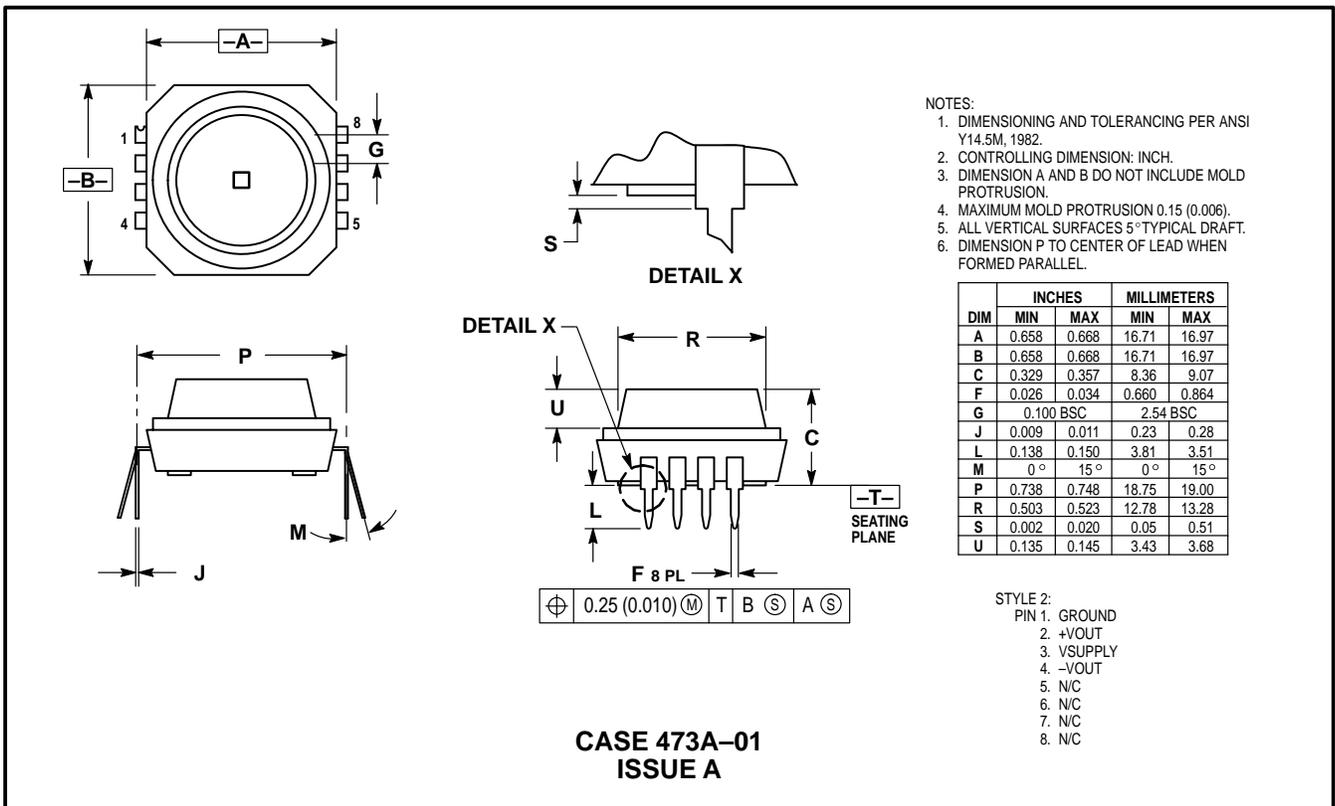
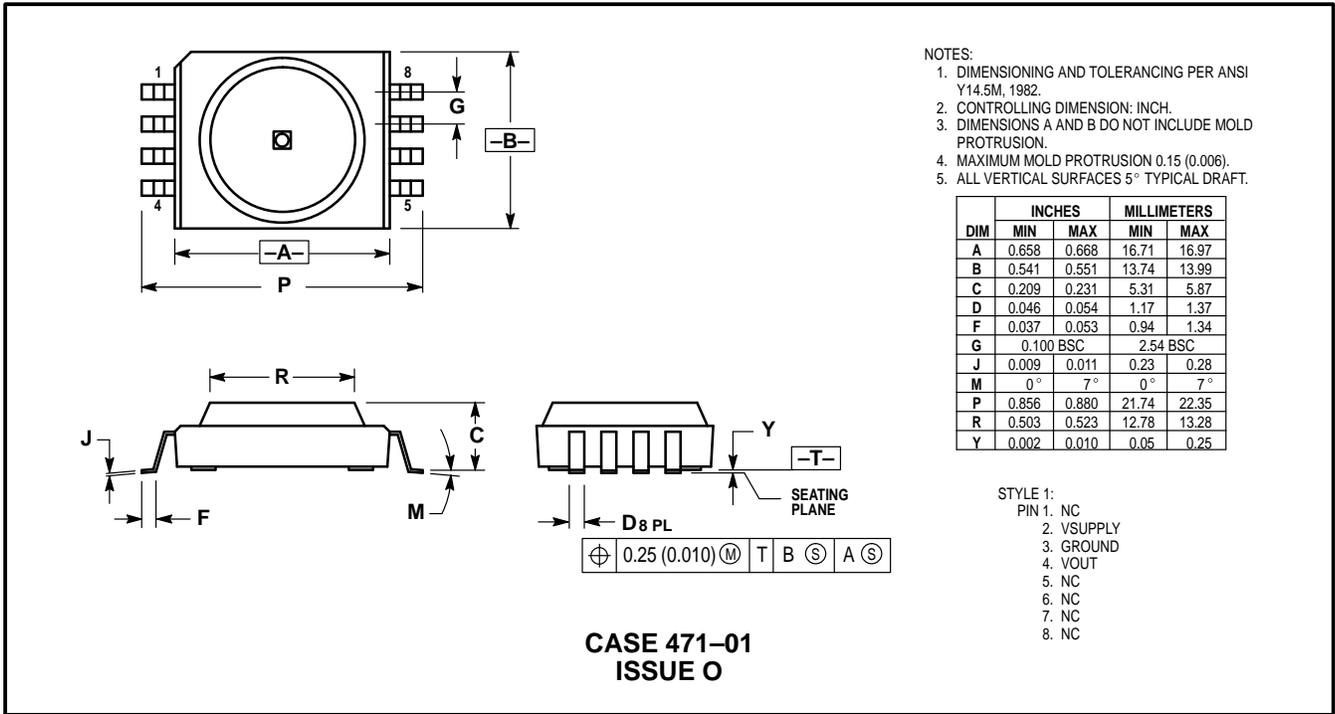


**PRESSURE SIDE PORTED (AS, GS)**



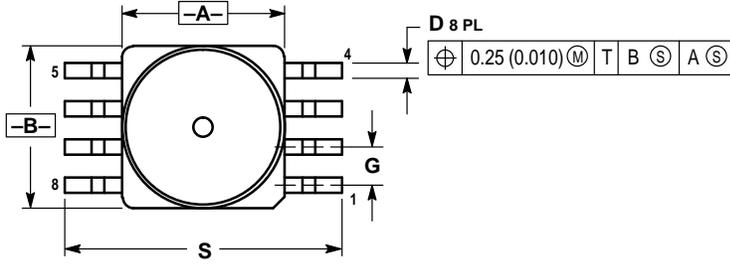
**PRESSURE SIDE PORTED (ASX, GSX)**

**MPX4100A MPXS4100A MPXT4100A MPXA4100A SERIES**  
**SURFACE MOUNT PACKAGE DIMENSIONS**



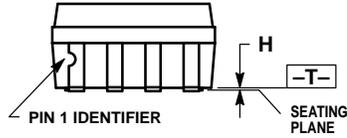
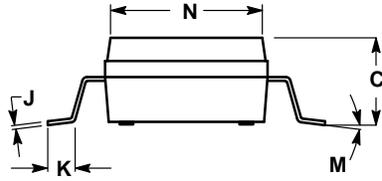
**MPX4100A MPXS4100A MPXT4100A MPXA4100A SERIES**

**SMALL OUTLINE PACKAGE DIMENSIONS**



- NOTES:
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
  2. CONTROLLING DIMENSION: INCH.
  3. DIMENSION A AND B DO NOT INCLUDE MOLD PROTRUSION.
  4. MAXIMUM MOLD PROTRUSION 0.15 (0.006).
  5. ALL VERTICAL SURFACES 5° TYPICAL DRAFT.

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.415	0.425	10.54	10.79
B	0.415	0.425	10.54	10.79
C	0.212	0.230	5.38	5.84
D	0.038	0.042	0.96	1.07
G	0.100 BSC		2.54 BSC	
H	0.002	0.010	0.05	0.25
J	0.009	0.011	0.23	0.28
K	0.061	0.071	1.55	1.80
M	0°	7°	0°	7°
N	0.405	0.415	10.29	10.54
S	0.709	0.725	18.01	18.41



**CASE 482-01  
ISSUE O**

**NOTES**

## MPX4100A MPXS4100A MPXT4100A MPXA4100A SERIES

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