#### Data Sheet No. PD 10038D

# International TOR Rectifier

# **Series PVT312**

Microelectronic Power IC HEXFET® Power MOSFET Photovoltaic Relay Single Pole, Normally Open, 0-250V, 190mA AC/DC

# **General Description**

The PVT312 Photovoltaic Relay is a single-pole, normally open solid-state relay that can replace electromechanical relays in many applications. It utilizes International Rectifier's proprietary HEXFET power MOSFET as the output switch, driven by an integrated circuit photovoltaic generator of novel construction. The output switch is controlled by radiation from a GaAlAs light emitting diode (LED) which is optically isolated from the photovoltaic generator.

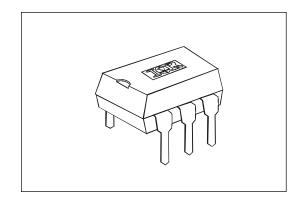
This SSR is specifically designed for telecom applications. PVT312L employs an active current-limiting circuitry enabling it to withstand current surge transients.

PVT312 Relays are packaged in a 6-pin, molded DIP package with either through-hole or surface mount ("gull-wing") terminals. It is available in standard plastic shipping tubes or on tape-and-reel. Please refer to the Part Identification information opposite.

#### **Features**

- HEXFET Power MOSFET output
- Bounce-free operation
- 4,000 V<sub>RMS</sub> I/O isolation
- Load current limiting
- Linear AC/DC operation
- Solid-State Reliability
- UL recognized and BABT certified
- ESD Tolerance:

4000V Human Body Model 500V Machine Model



# **Applications**

- On/Off Hook switch
- Dial-Out relay
- Ring injection relay
- Ground start
- General switching

#### Part Identification

PV1312L	current limit, through-hole
PVT312LS	current limit, surface-mount
PVT312LS-T	current limit, surface-mount,
	tape and reel
PVT312	no current limit, through-hole
PVT312S	no current limit, surface-
	mount
PVT312S-T	no current limit, surface-
	mount, tape and reel

# **Series PVT312**

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Rectifier

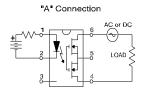
# **Electrical Specifications** (-40°C $\leq$ T<sub>A</sub> $\leq$ +85°C unless otherwise specified)

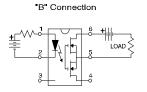
INPUT CHARACTERISTICS	Part Nu	Part Numbers		
	PVT312L	PVT312		
Minimum Control Current (see figures 1 and 2)	2.	2.0		
Maximum Control Current for Off-State Resistance @ T <sub>A</sub> =+25°C	0.	0.4		
Control Current Range (Caution: current limit input LED, see figure 6)	2.0 t	2.0 to 25		
Maximum Reverse Voltage	7.	7.0		

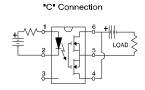
OUTPUT CHARACTERISTICS	PVT312L		PVT312	
Operating Voltage Range		0 to ±250		V(DC or AC peak)
Maximum Load Current @ T <sub>A</sub> =+40°C, 5mA Control (see figures 1 and 2)				
A Connection	17	0	190	mA (AC or DC)
B Connection	19	00	210	mA (DC)
C Connection	30	00	320	mA (DC)
Maximum On-State Resistance @T <sub>A</sub> =+25°C for 50mA pulsed load				
5mA Control (see figure4)				
A Connection	1:	5	10	Ω
B Connection	8	3	5.5	Ω
C Connection	4.2	25	3	Ω
Maximum Off-State Leakage @T <sub>A</sub> =+25°C, ±250V (see figure 5)	1.0		μA	
Current Limit @T <sub>A</sub> =+25°C, 5mA Control				
Connection:	Α	С		
Minimum	190	330	n/a	mA
Maximum	300	560	n/a	mA
Maximum Turn-On Time @T <sub>A</sub> =+25°C (see figure 7)		3.0		ms
for 50mA, 100 V <sub>DC</sub> load, 5mA Control				
Maximum Turn-Off Time @T <sub>A</sub> =+25°C (See Fig. 6)		0.5		ms
For 50mA, 100 V <sub>DC</sub> load, 5mA Control				
Maximum Output Capacitance @ 50VDC		50		pF
	1			l l

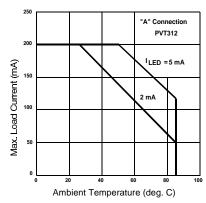
GENERAL CHARACTERISTIC	ALL MODELS		
Minimum Dielectric Strength, Input-Output		4000	V <sub>RMS</sub>
Minimum Insulation Resistance, Input-Output @T <sub>A</sub> =+25°C, 50%RH, 100V <sub>DC</sub>		1012	Ω
Maximum Capacitance, Input-Output		1.0	pF
Maximum Pin Soldering Temperature (10 seconds maximum)		+260	°C
Ambient Temperature Range:	Operating	-40 to +85	°C
	Storage	-40 to +100	

# **Connection Diagrams**









**Figure 1. Typical Current Derating Curves** 

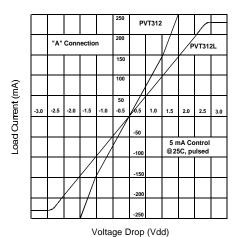


Figure 3. Linearity Characteristics

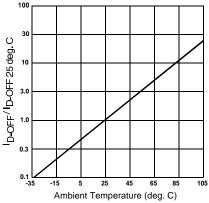
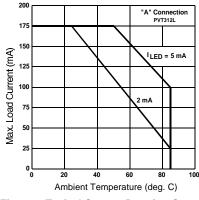


Figure 5. Typical Normalized Off-State Leakage



**Figure 2. Typical Current Derating Curves** 

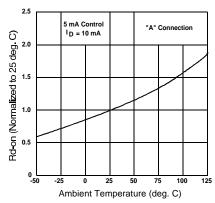


Figure 4. Typical Normalized On-Resistance

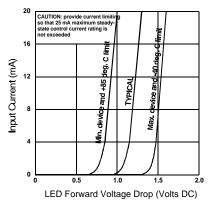
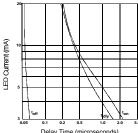


Figure 6. Input Characteristics (Current Controlled)



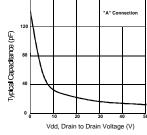
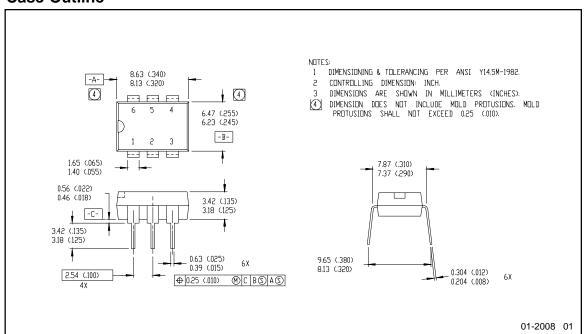


Figure 7. Typical Delay Times

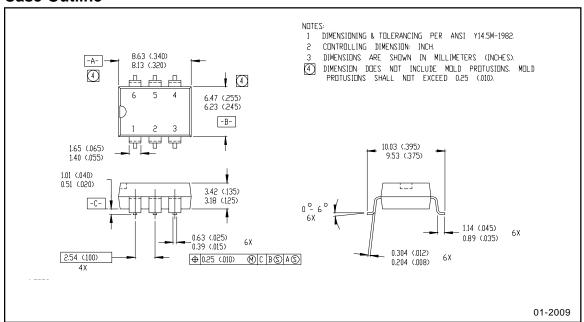
Figure 8. Delay Time Definitions

Figure 9. Typical Output Capacitance

## **Case Outline**



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Data and specifications subject to change without notice. 8/1/2000